National Semiconductor

May 1999

LM6165/LM6265/LM6365 High Speed Operational Amplifier

General Description

The LM6165 family of high-speed amplifiers exhibits an excellent speed-power product in delivering 300 V/µs and 725 MHz GBW (stable for gains as low as +25) with only 5 mA of supply current. Further power savings and application convenience are possible by taking advantage of the wide dynamic range in operating supply voltage which extends all the way down to +5V.

These amplifiers are built with National's VIP™ (Vertically Integrated PNP) process which produces fast PNP transistors that are true complements to the already fast NPN devices. This advanced junction-isolated process delivers high speed performance without the need for complex and expensive dielectric isolation.

Features

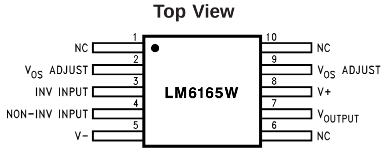
- High slew rate: 300 V/µs
- High GBW product: 725 MHz
- Low supply current: 5 mA
- Fast settling: 80 ns to 0.1%
- Low differential gain: <0.1%
- Low differential phase: <0.1°
- Wide supply range: 4.75V to 32V
- Stable with unlimited capacitive load

Applications

- Video amplifier
- Wide-bandwidth signal conditioning
- Radar
- Sonar

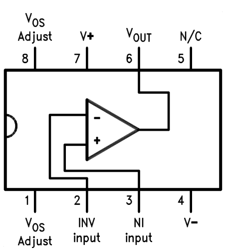
Connection Diagrams

10-Lead Flatpak
Top View



DS009152-14

Order Number LM6165W/883
See NS Package Number W10A



DS009152-8

Order Number LM6165J/883
See NS Package Number J08A
Order Number LM6365M
See NS Package Number M08A
Order Number LM6265N or LM6365N
See NS Package Number N08E

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 DS009152

Connection Diagrams (Continued)

Temperature Range			Package	NSC Drawing
Military $-55^{\circ}C \leq T_A \leq +125^{\circ}C$	**Industrial** $-25^{\circ}C \leq T_A \leq +85^{\circ}C$	**Commercial** $0^{\circ}C \leq T_A \leq +70^{\circ}C$		
	LM6265N	LM6365N	8-Pin Molded DIP	N08E
LM6165J/883 5962-8962501PA			8-Pin Ceramic DIP	J08A
		LM6365M	8-Pin Molded Surface Mt.	M08A
LM6165WG/883 5962-8962501XA			10-Lead Ceramic SOIC	WG10A
LM6165W883 5962-8962501HA			10-Pin Ceramic Flatpak	W10A

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Supply Voltage ($V^+ - V^-$)	36V
Differential Input Voltage (Note 7)	±8V
Common-Mode Voltage Range (Note 11)	(V^+ − 0.7V) to (V^- + 0.7V)
Output Short Circuit to GND (Note 2)	Continuous
Soldering Information	
Dual-In-Line Package (N, J)	
Soldering (10 sec.)	260˚C
Small Outline Package (M)	
Vapor Phase (60 sec.)	215˚C
Infrared (15 sec.)	220˚C

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

Storage Temp Range	−65˚C to +150˚C
Max Junction Temperature (Note 3)	150˚C
ESD Tolerance (Notes 7, 8)	±700V

Operating Ratings

Temperature Range (Note 3)	
LM6165, LM6165J/883	−55˚C ≤ T_J ≤ +125˚C
LM6265	−25˚C ≤ T_J ≤ +85˚C
LM6365	0˚C ≤ T_J ≤ +70˚C
Supply Voltage Range	4.75V to 32V

DC Electrical Characteristics

The following specifications apply for Supply Voltage = ±15V, V_{CM} = 0, R_L ≥ 100 kΩ and R_S = 50Ω unless otherwise noted. **Boldface** limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}; all other limits $T_A = T_J$ = 25˚C.

Symbol	Parameter	Conditions	Typ	LM6165 Limit (Notes 4, 12)	LM6265 Limit (Note 4)	LM6365 Limit (Note 4)	Units
V_{OS}	Input Offset Voltage		1	3 **4**	3 **4**	6 **7**	mV Max
V_{OS} Drift	Input Offset Voltage Average Drift		3				µV/˚C
I_b	Input Bias Current		2.5	3 **6**	3 **5**	5 **6**	µA Max
I_{OS}	Input Offset Current		150	350 **800**	350 **600**	1500 **1900**	nA Max
I_{OS} Drift	Input Offset Current Average Drift		0.3				nA/˚C
R_{IN}	Input Resistance	Differential	20				kΩ
C_{IN}	Input Capacitance		6.0				pF
A_{VOL}	Large Signal Voltage Gain (Note 10)	V_{OUT} = ±10V, R_L = 2 kΩ	10.5	7.5 **5.0**	7.5 **6.0**	5.5 **5.0**	V/mV Min
		R_L = 10 kΩ	38				
V_{CM}	Input Common-Mode Voltage Range	Supply = ±15V	+14.0	+13.9 **+13.8**	+13.9 **+13.8**	+13.8 **+13.7**	V Min
			−13.6	−13.4 **−13.2**	−13.4 **−13.2**	−13.3 **−13.2**	V Min
		Supply = +5V (Note 5)	4.0	3.9 **3.8**	3.9 **3.8**	3.8 **3.7**	V Min
			1.4	1.6 **1.8**	1.6 **1.8**	1.7 **1.8**	V Max
CMRR	Common-Mode Rejection Ratio	−10V ≤ V_{CM} ≤ +10V	102	88 **82**	88 **84**	80 **78**	dB Min
PSRR	Power Supply Rejection Ratio	±10V ≤ $V^{\pm}$ ≤ ±16V	104	88 **82**	88 **84**	80 **78**	dB Min

DC Electrical Characteristics (Continued)

The following specifications apply for Supply Voltage = ±15V, V_{CM} = 0, R_L ≥ 100 kΩ and R_S = 50Ω unless otherwise noted. **Boldface** limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}; all other limits $T_A = T_J$ = 25˚C.

Symbol	Parameter	Conditions	Typ	LM6165 Limit (Notes 4, 12)	LM6265 Limit (Note 4)	LM6365 Limit (Note 4)	Units
V_O	Output Voltage Swing	Supply = ±15V, R_L = 2 kΩ	+14.2	+13.5	+13.5	+13.4	V
				+13.3	**+13.3**	**+13.3**	Min
			−13.4	−13.0	−13.0	−12.9	V
				−12.7	**−12.8**	**−12.8**	Min
		Supply = +5V R_L = 2 kΩ (Note 5)	4.2	3.5	3.5	3.4	V
				3.3	**3.3**	**3.3**	Min
			1.3	1.7	1.7	1.8	V
				2.0	**1.9**	**1.9**	Max
	Output Short Circuit Current	Source	65	30	30	30	mA
				20	**25**	**25**	Min
		Sink	65	30	30	30	mA
				20	**25**	**25**	Min
I_S	Supply Current		5.0	6.5	6.5	6.8	mA
				6.8	**6.7**	**6.9**	Max

AC Electrical Characteristics

The following specifications apply for Supply Voltage = ±15V, V_{CM} = 0, R_L ≥ 100 kΩ and R_S = 50Ω unless otherwise noted. **Boldface** limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}; all other limits $T_A = T_J$ = 25˚C. (Note 6)

Symbol	Parameter	Conditions	Typ	LM6165 Limit (Notes 4, 12)	LM6265 Limit (Note 4)	LM6365 Limit (Note 4)	Units
GBW	Gain Bandwidth	F = 20 MHz	725	575	575	500	MHz
				350			Min
	Product	Supply = ±5V	500				
SR	Slew Rate	A_V = +25 (Note 9)	300	200	200	200	V/µs
				180			Min
		Supply = ±5V	200				
PBW	Power Bandwidth Product	V_{OUT} = 20 V_{PP}	4.5				MHz
t_S	Settling Time	10V Step to 0.1% A_V = −25, R_L = 2 kΩ	80				ns
ϕ_m	Phase Margin	A_V = +25	45				Deg
A_D	Differential Gain	NTSC, A_V = +25	<0.1				%
ϕ_D	Differential Phase	NTSC, A_V = +25	<0.1				Deg
e_{np-p}	Input Noise Voltage	F = 10 kHz	5				$nV/\sqrt{Hz}$
i_{np-p}	Input Noise Current	F = 10 kHz	1.5				$pA/\sqrt{Hz}$

Note 1: “Absolute Maximum Ratings” indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

Note 2: Continuous short-circuit operation at elevated ambient temperature can result in exceeding the maximum allowed junction temperature of 150˚C.

Note 3: The typical junction-to-ambient thermal resistance of the molded plastic DIP (N) is 105˚C/Watt, and the molded plastic SO (M) package is 155˚C/Watt, and the cerdip (J) package is 125˚C/Watt. All numbers apply for packages soldered directly into a printed circuit board.

Note 4: All limits guaranteed by testing or correlation.

Note 5: For single supply operation, the following conditions apply: V+ = 5V, V− = 0V, V_{CM} = 2.5C, V_{OUT} = 2.5V. Pin 1 & Pin 8 (V_{OS} Adjust) are each connected to Pin 4 (V−) to realize maximum output swing. This connection will degrade V_{OS}.

Note 6: C_L ≤ 5 pF.

Note 7: In order to achieve optimum AC performance, the input stage was designed without protective clamps. Exeeding the maximum differential input voltage results in reverse breakdown of the base-emitter junction of one of the input transistors and probable degradation of the input parameters (especially V_{OS}, I_{OS}, and Noise).

AC Electrical Characteristics (Continued)

Note 8: The average voltage that the weakest pin combinations (those involving Pin 2 or Pin 3) can withstand and still conform to the datasheet limits. The test circuit used consists of the human body model of 100 pF in series with 1500Ω.

Note 9: V_{IN} = 0.8V step. For supply = ±5V, V_{IN} = 0.2V step.

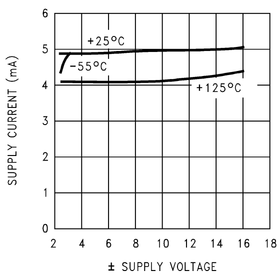
Note 10: Voltage Gain is the total output swing (20V) divided by the input signal required to produce that swing.

Note 11: The voltage between V^+ and either input pin must not exceed 36V.

Note 12: A military RETS electrical test specification is available on request. At the time of printing, the LM6165J/883 RETS spec complied with the Boldface limits in this column. The LM6165J/883 may also be procured as Standard Military Drawing #5962-8962501PA.

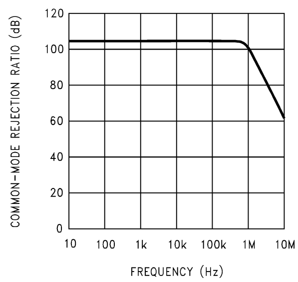
Typical Performance Characteristics R_L = 10 kΩ, T_A = 25˚C unless otherwise specified

Supply Current vs Supply Voltage



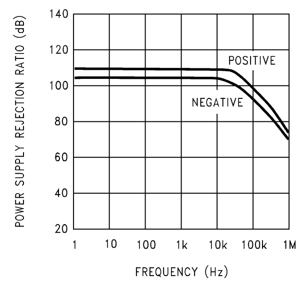
DS009152-16

Common-Mode Rejection Ratio



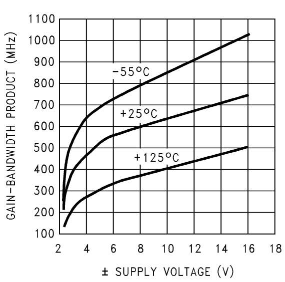
DS009152-17

Power Supply Rejection Ratio



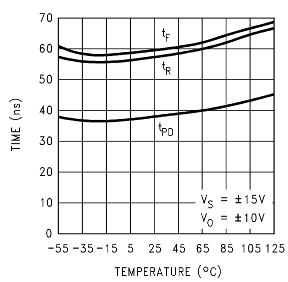
DS009152-18

Gain-Bandwidth Product



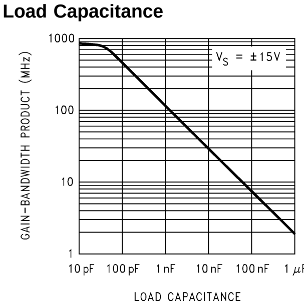
DS009152-19

Propagation Delay, Rise and Fall Times



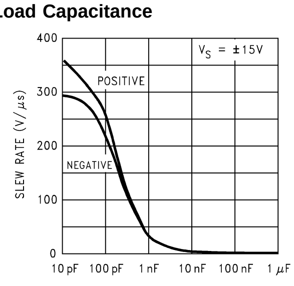
DS009152-20

Gain-Bandwidth Product vs Load Capacitance



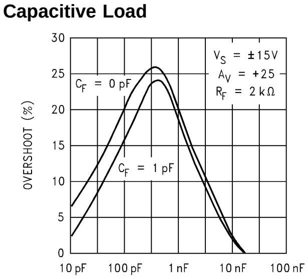
DS009152-21

Slew Rate vs Load Capacitance



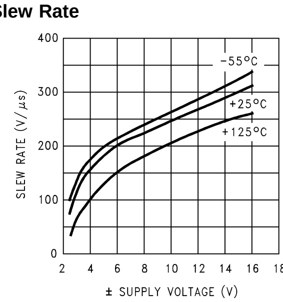
DS009152-22

Overshoot vs Capacitive Load



DS009152-23

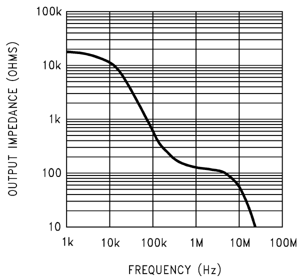
Slew Rate



DS009152-24

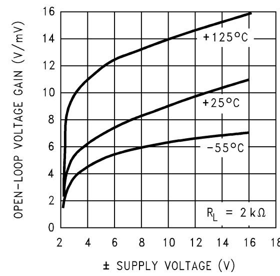
Typical Performance Characteristics R_L = 10 kΩ, T_A = 25˚C unless otherwise specified (Continued)

Output Impedance (Open-Loop)



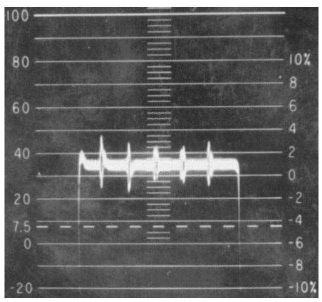
DS009152-25

Gain vs Supply Voltage



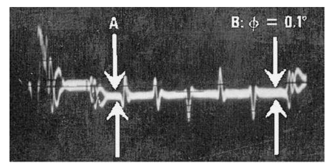
DS009152-26

Differential Gain (Note 13)



DS009152-6

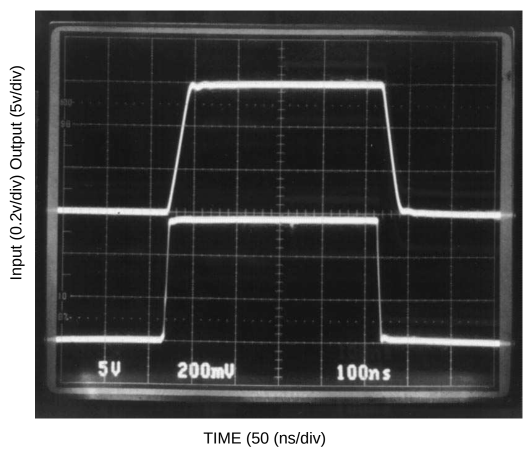
Differential Phase (Note 13)



DS009152-7

Note 13: Differential gain and differential phase measured for four series LM6365 op amps configured with gain of +25 (each output attenuated by 96%), in series with an LM6321 buffer. Error added by LM6321 is negligible. Test performed using Tektronix Type 520 NTSC test system.

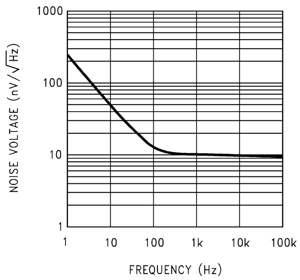
Step Response; Av = +25



DS009152-1

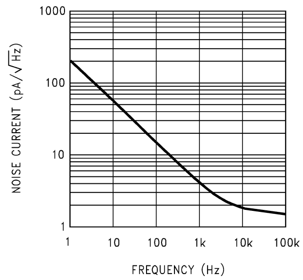
Typical Performance Characteristics R_L = 10 kΩ, T_A = 25˚C unless otherwise specified (Continued)

Input Noise Voltage



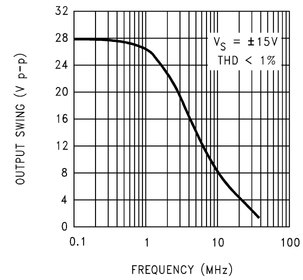
DS009152-27

Input Noise Current



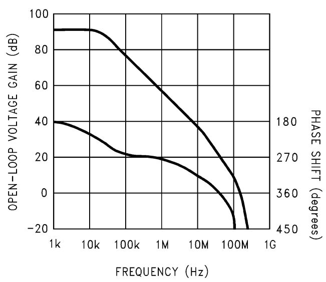
DS009152-28

Power Bandwidth



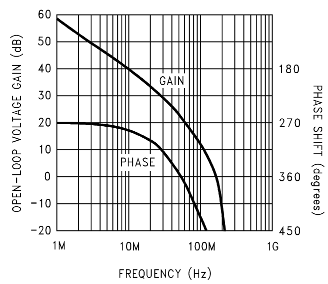
DS009152-29

Open-Loop Frequency Response



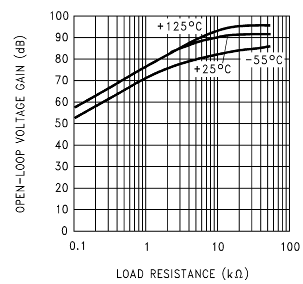
DS009152-30

Open-Loop Frequency Response



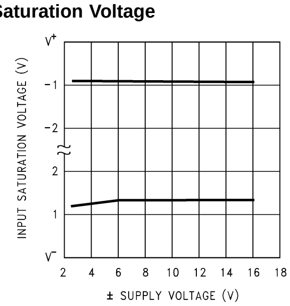
DS009152-31

Voltage Gain vs Load Resistance



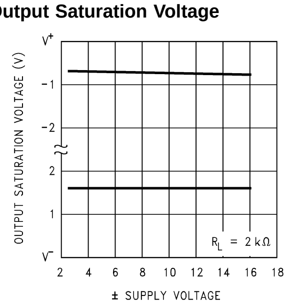
DS009152-32

Common-Mode Input Saturation Voltage



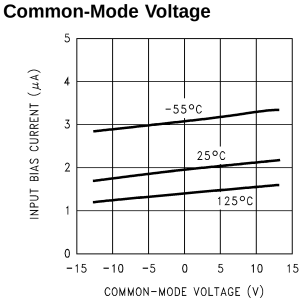
DS009152-33

Output Saturation Voltage



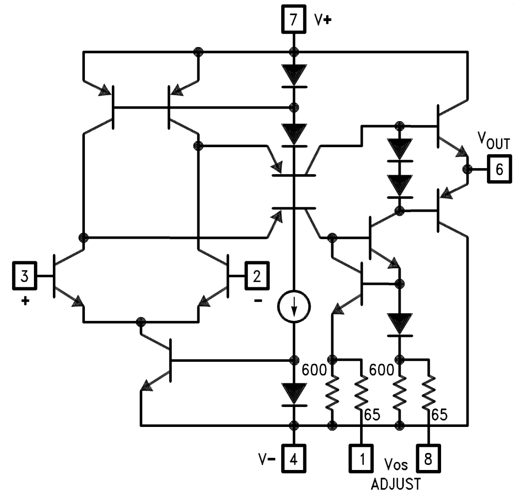
DS009152-34

Bias Current vs Common-Mode Voltage



DS009152-35

Simplified Schematic



DS009152-3

Application Tips

The LM6365 is stable for gains of 25 or greater. The LM6361 and LM6364, specified in separate datasheets, are compensated versions of the LM6365. The LM6361 is unity-gain stable, while the LM6364 is stable for gains as low as 5. The LM6361, and LM6364 have the same high slew rate as the LM6365, typically 300 V/µs.

To use the LM6365 for gains less than 25, a series resistor-capacitor network should be added between the input pins (as shown in the Typical Applications, Noise Gain Compensation) so that the high-frequency noise gain rises to at least 25.

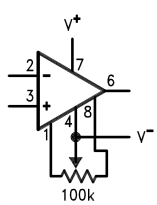
Power supply bypassing will improve stability and transient response of the LM6365, and is recommended for every design. 0.01 µF to 0.1 µF ceramic capacitors should be used (from each supply "rail" to ground); an additional 2.2 µF to 10 µF (tantalum) may be required for extra noise reduction.

Keep all leads short to reduce stray capacitance and lead inductance, and make sure ground paths are low-impedance, especially where heavier currents will be flowing. Stray capacitance in the circuit layout can cause signal coupling between adjacent nodes, and can cause circuit gain to unintentionally vary with frequency.

Breadboarded circuits will work best if they are built using generic PC boards with a good ground plane. If the op amps are used with sockets, as opposed to being soldered into the circuit, the additional input capacitance may degrade circuit performance.

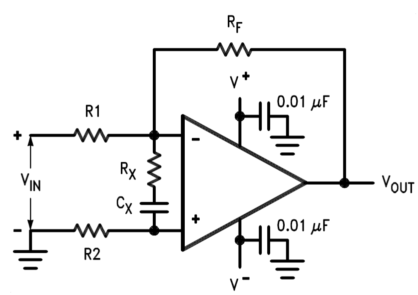
Typical Applications

Offset Voltage Adjustment



DS009152-11

Noise-Gain Compensation

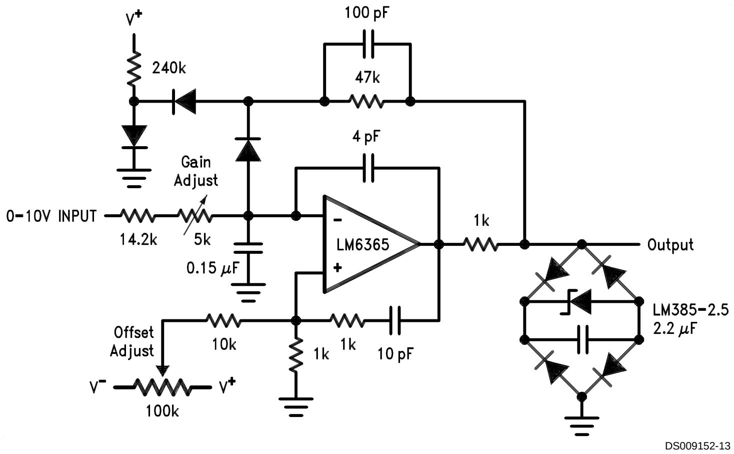


DS009152-12

$R_X C_X \geq 1/(2\pi \cdot 25\ \text{MHz})$
$[R1 + R_F (1 + R1/R2)] = 25\ R_X$

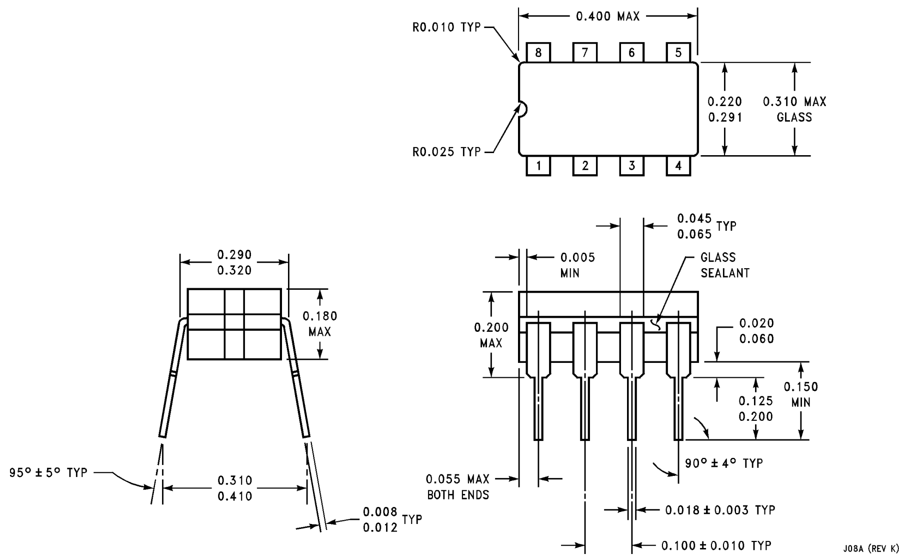
Typical Applications (Continued)

1 MHz Voltage-to-Frequency Converter
(f_{OUT} = 1 MHz for V_{IN} = 10V)

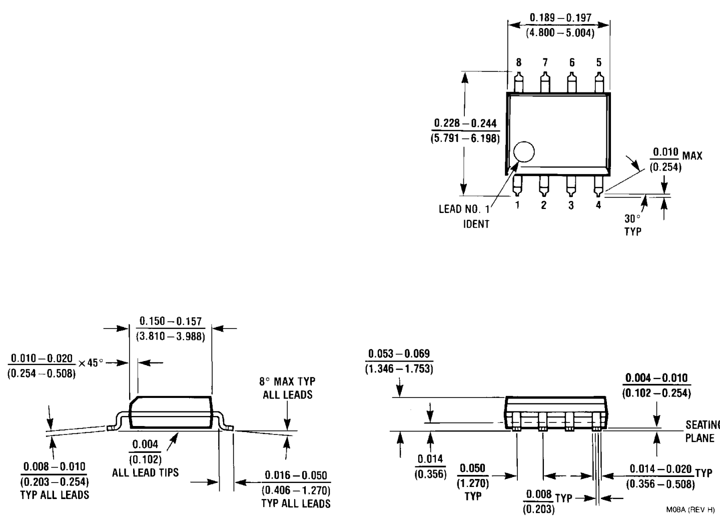


All diodes 1N914

Physical Dimensions inches (millimeters) unless otherwise noted

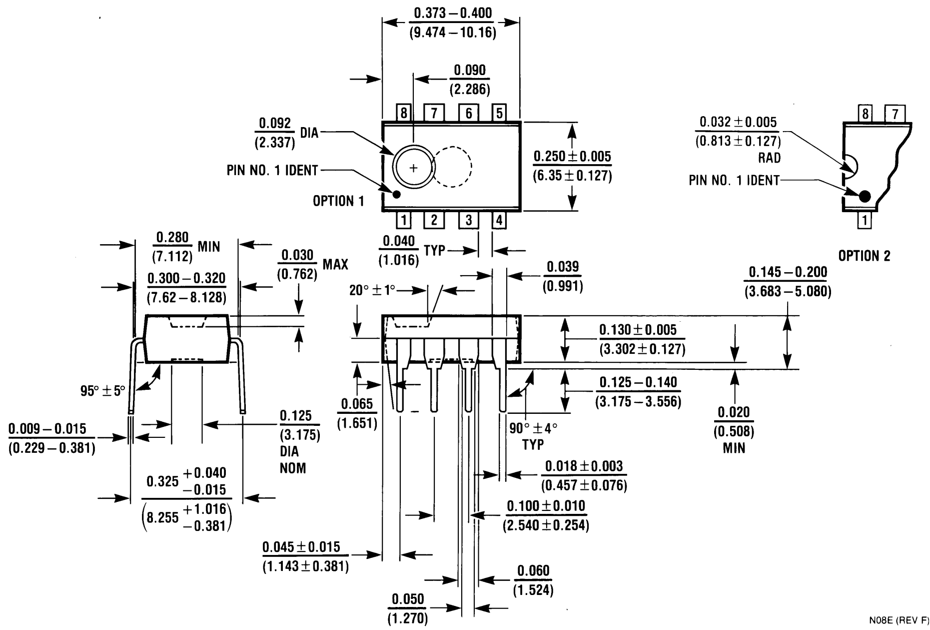


Ceramic Dual-In-Line Package (J)
Order Number LM6165J/883
NS Package Number J08A

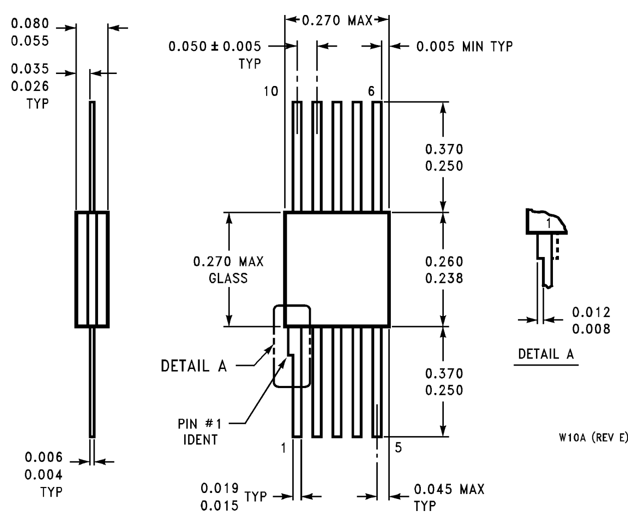


Molded Package SO (M)
Order Number LM6365M
NS Package Number M08A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



Molded Dual-In-Line Package (N)
Order Number LM6265N or LM6365N
NS Package Number N08E



10-Pin Ceramic Flatpak
Order Number LM6165W/883
NS Package Number W10A

Notes

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