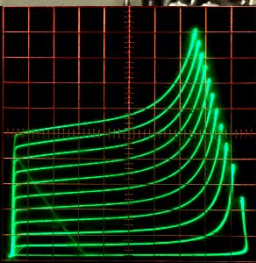
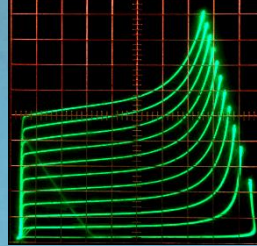
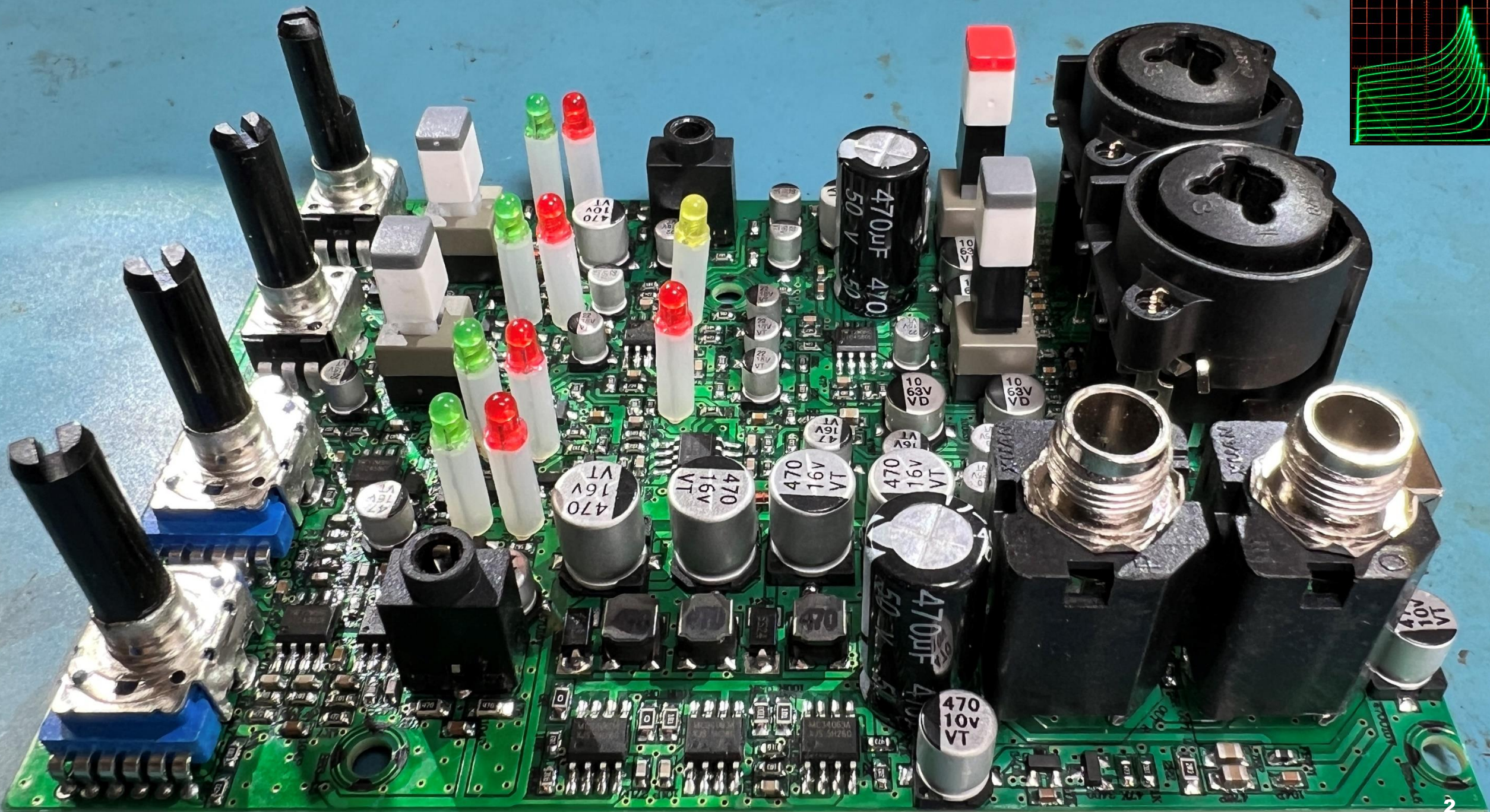
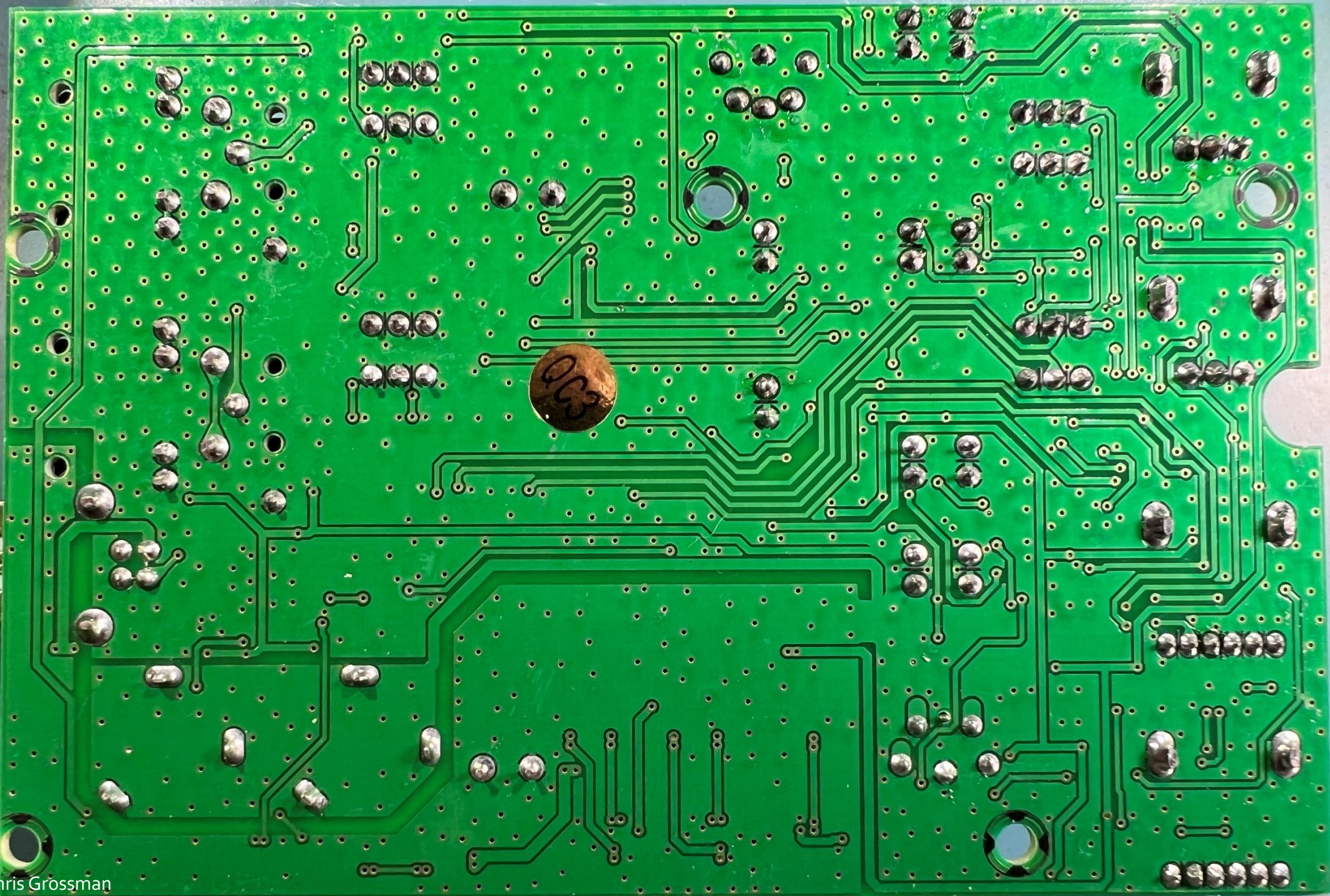
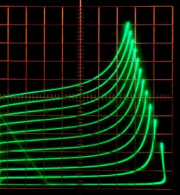
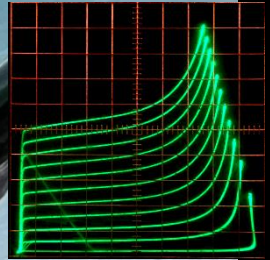
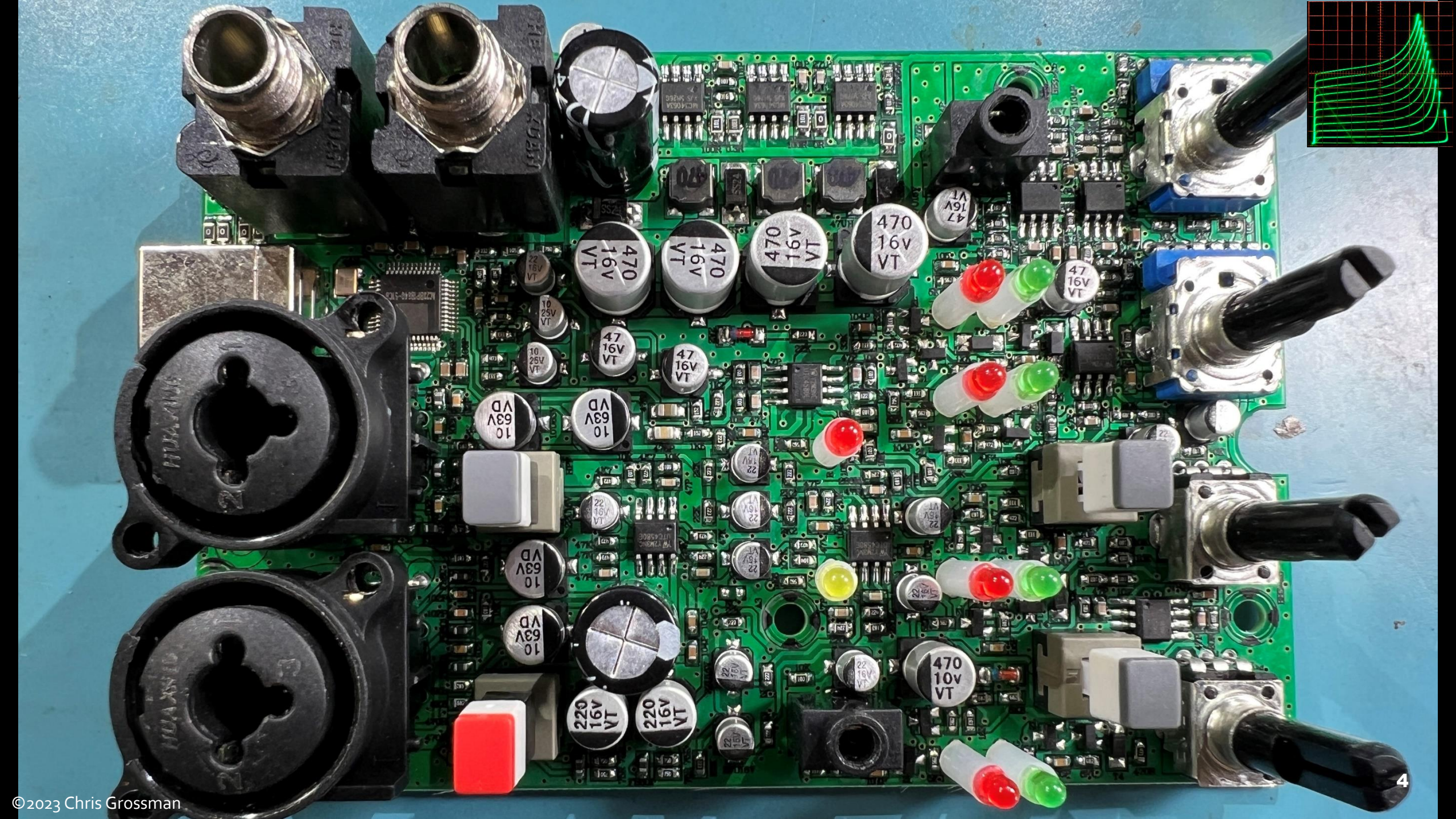


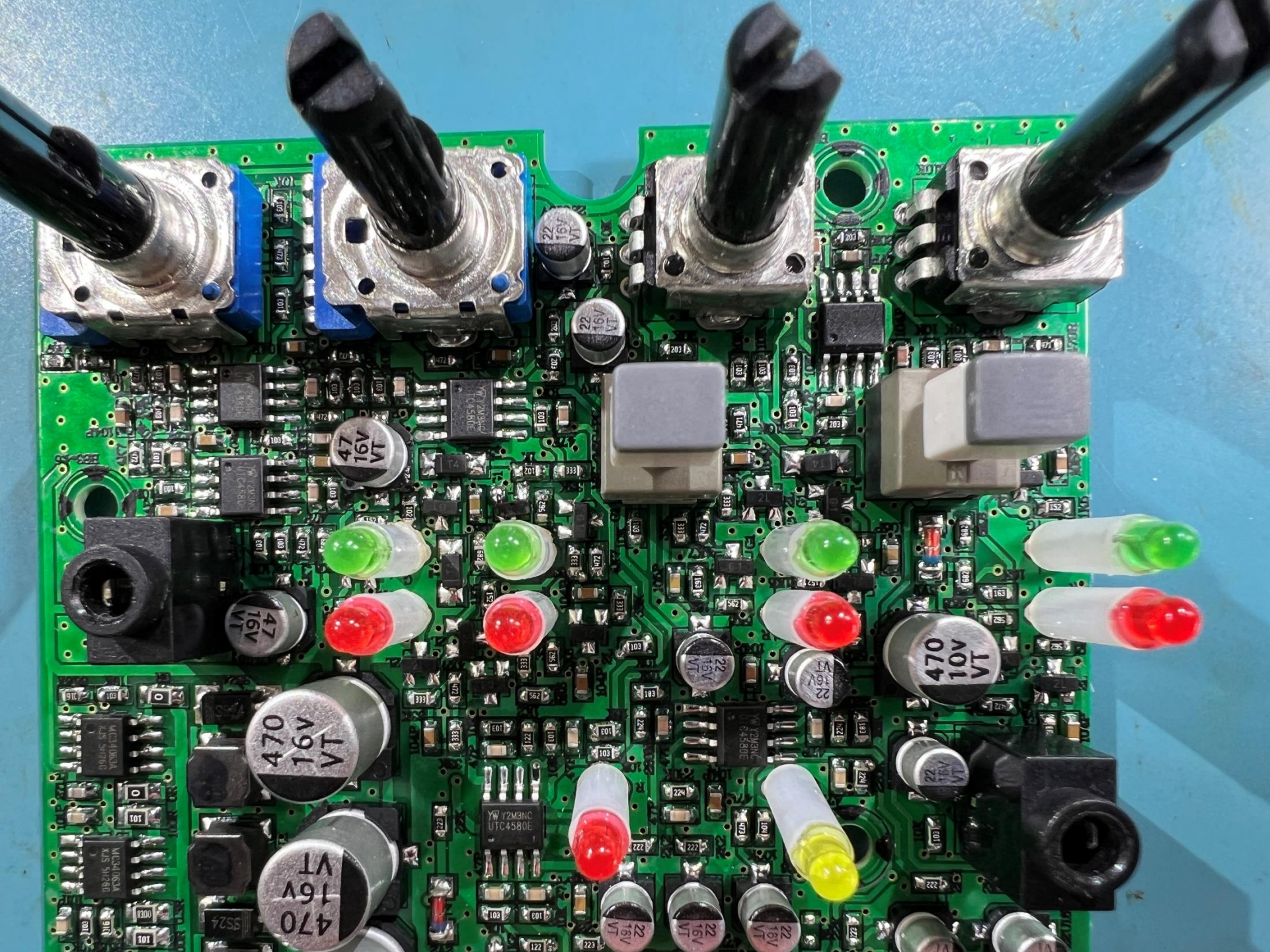
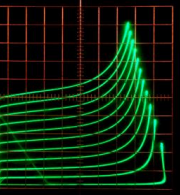
Teyun Q-12 Teardown

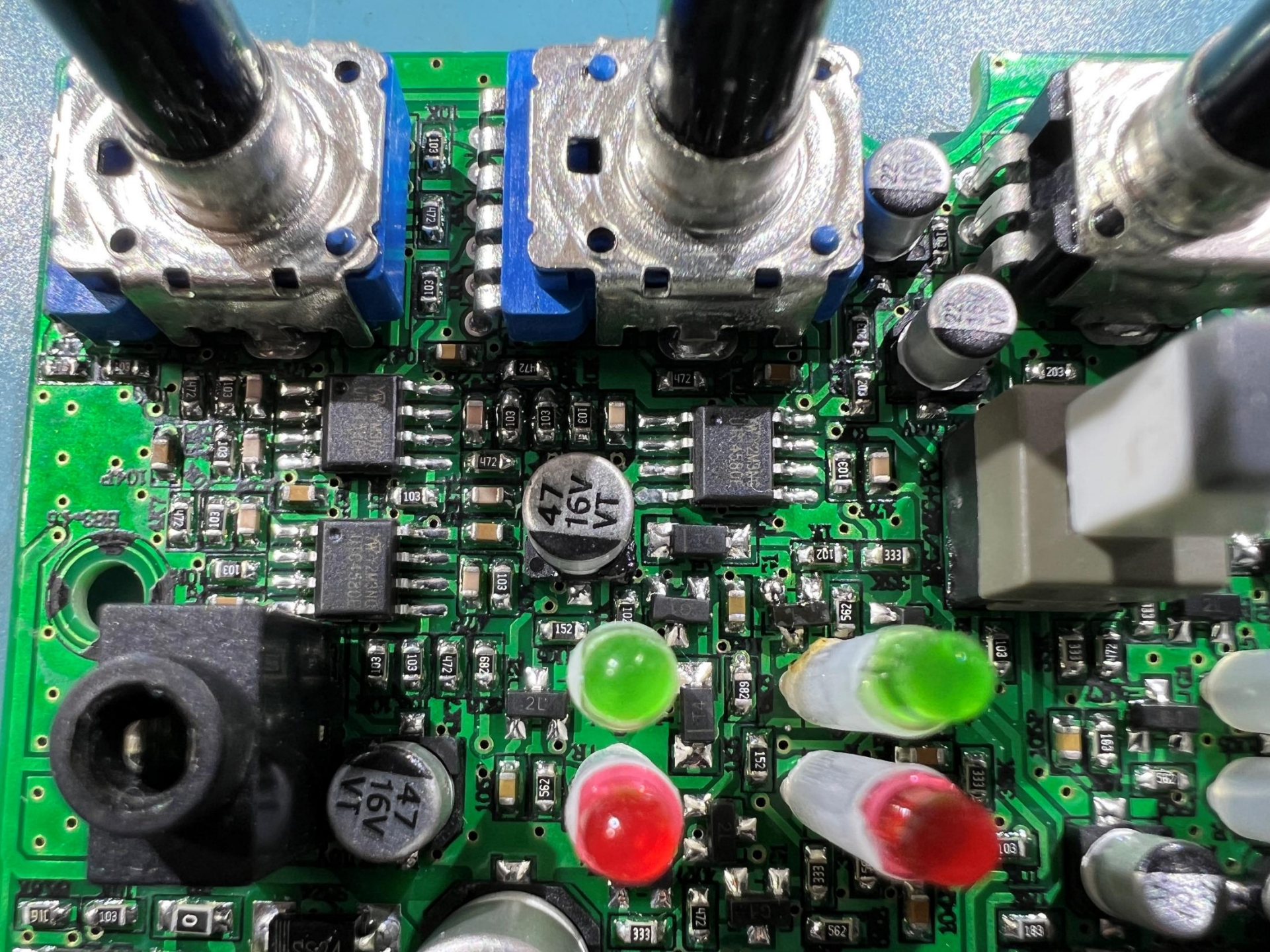
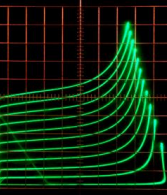


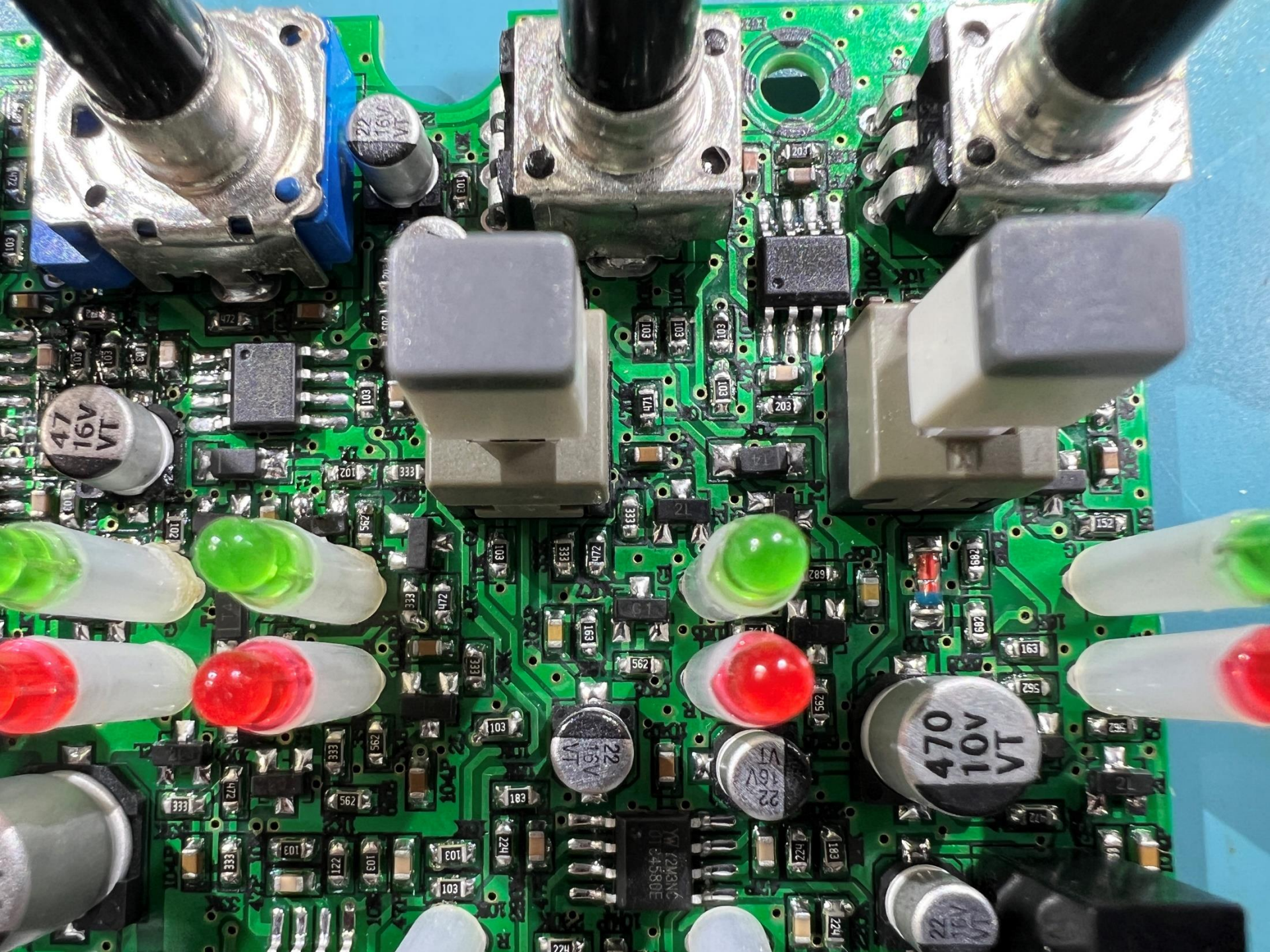
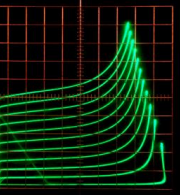


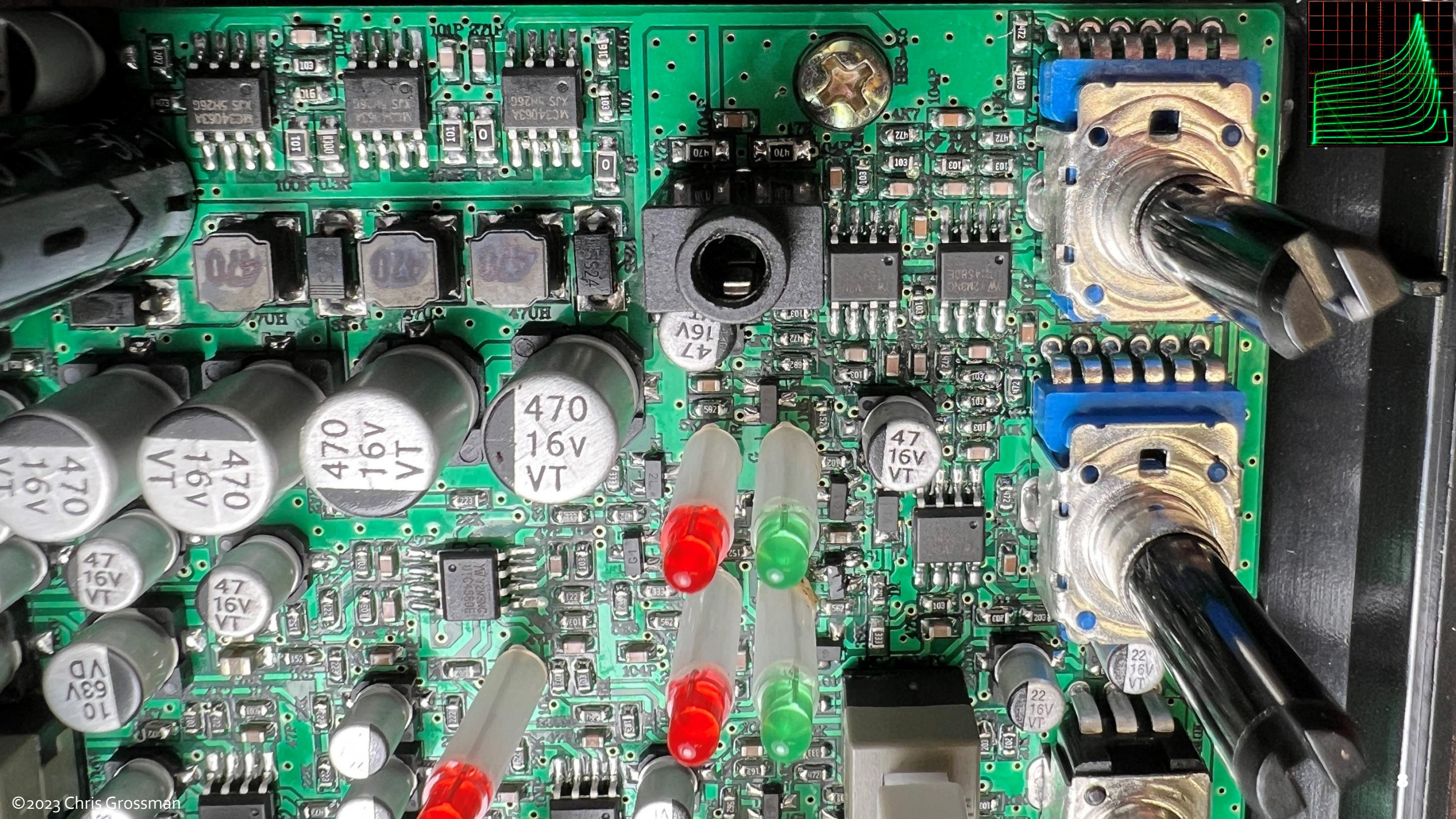


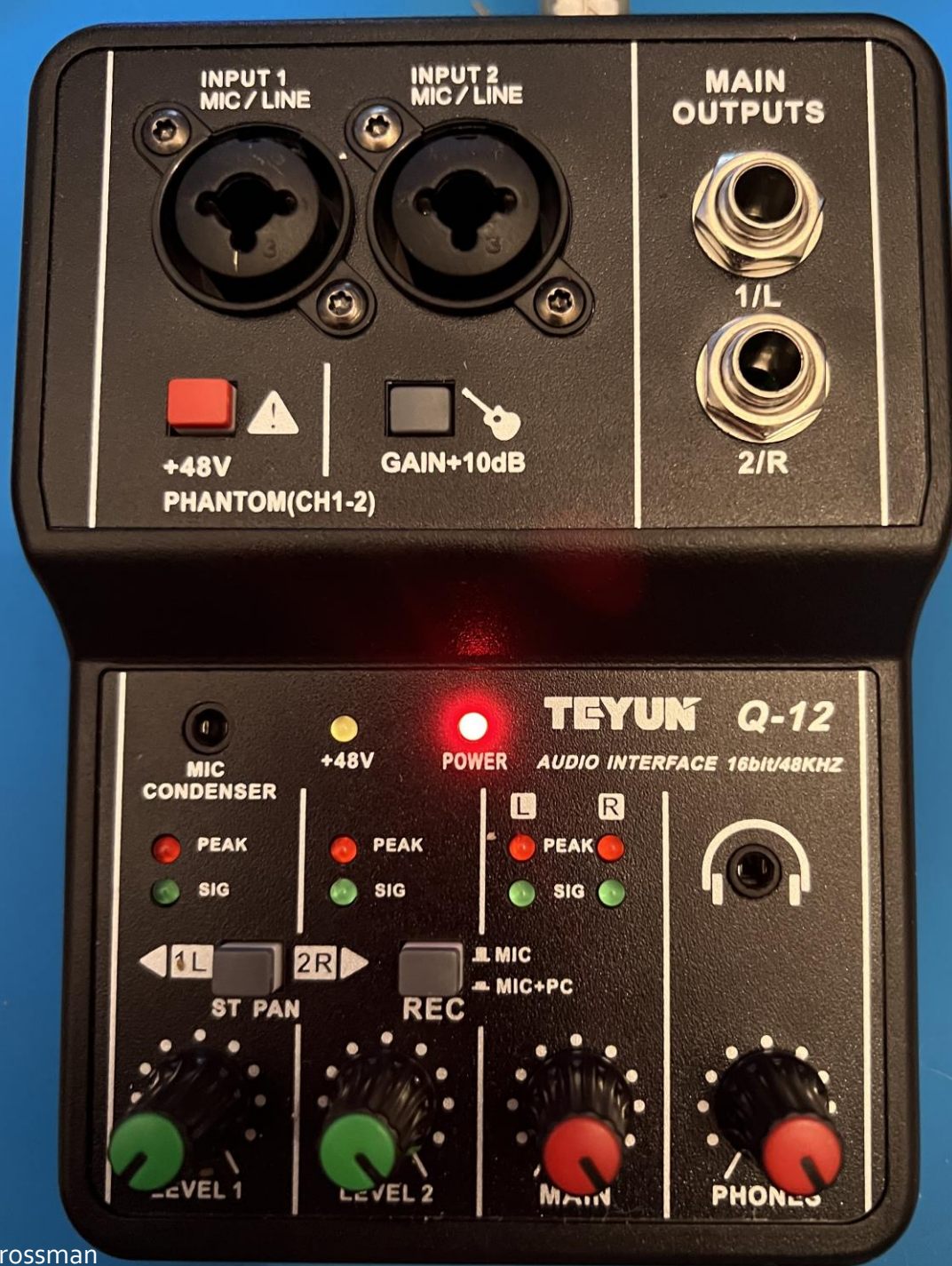
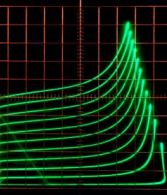


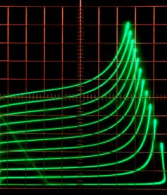


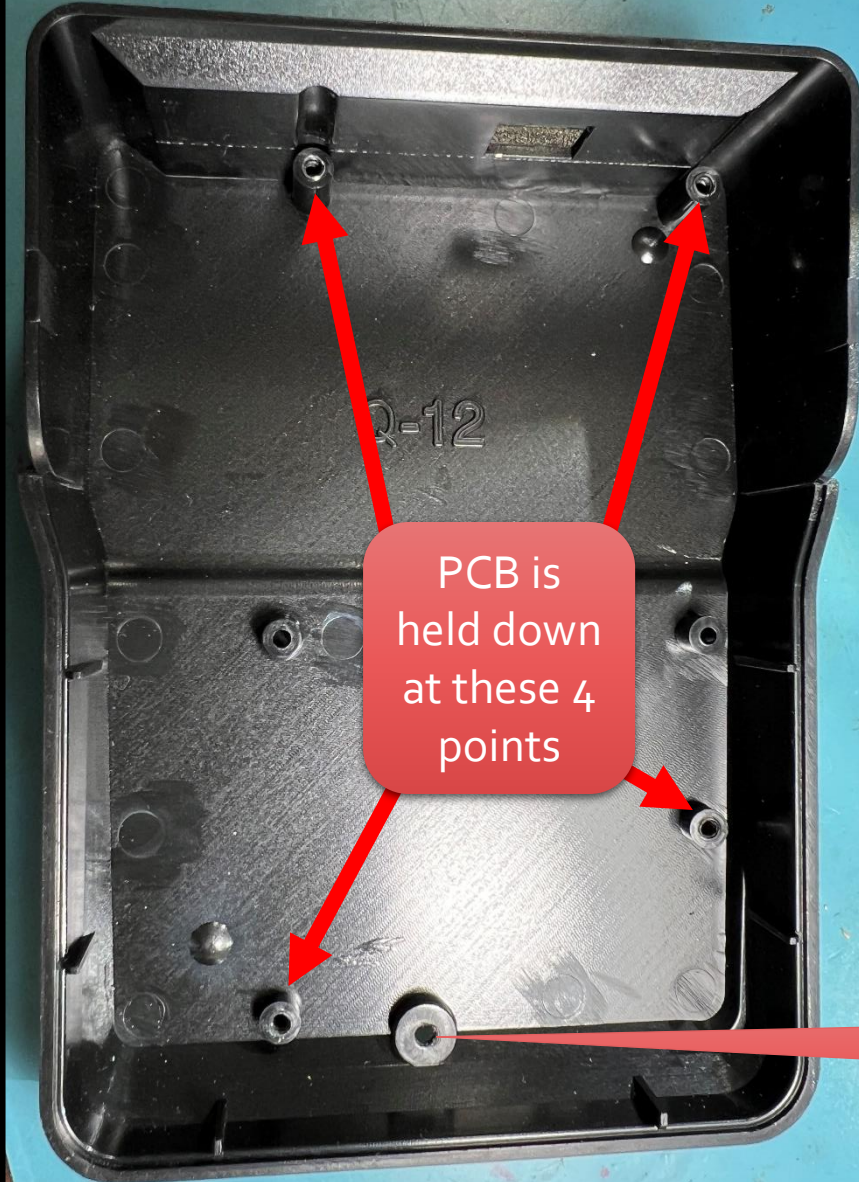
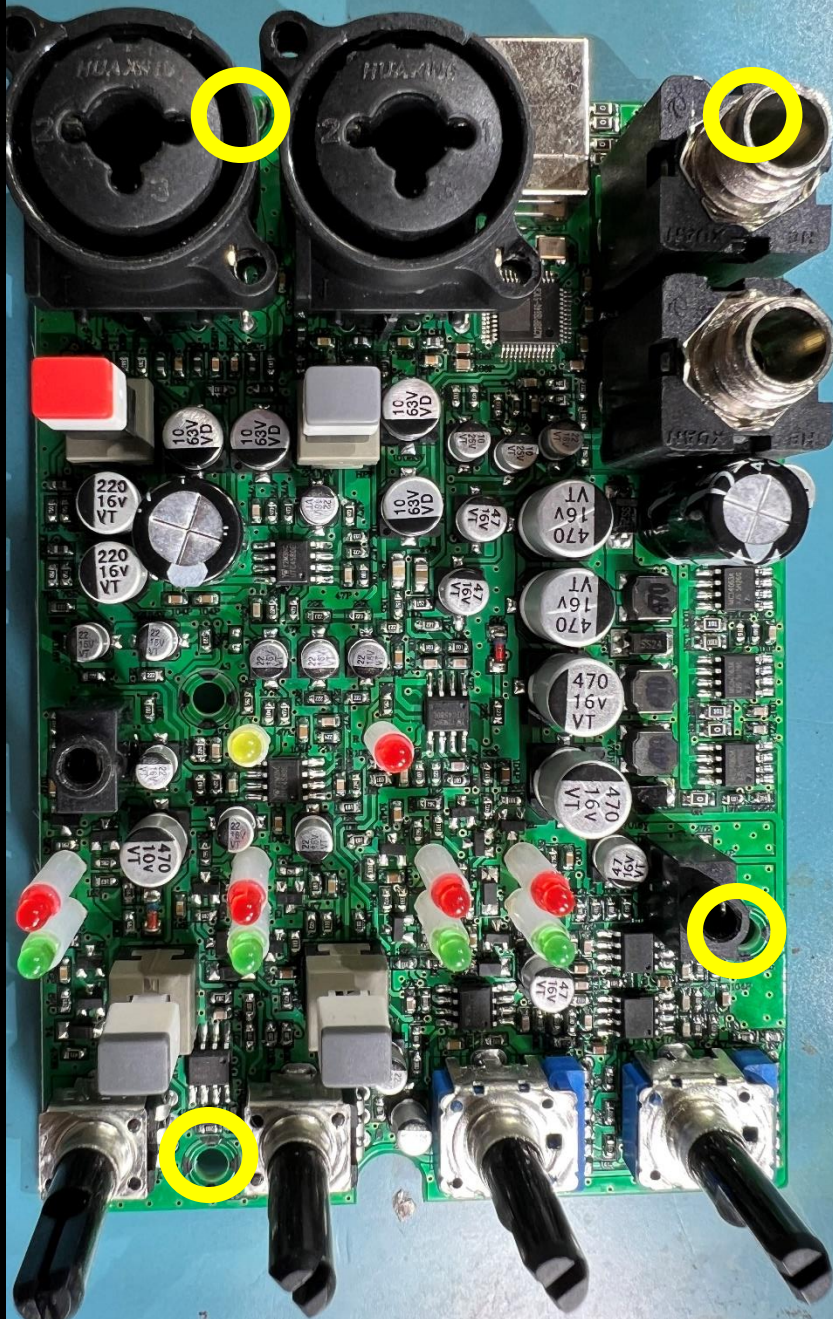
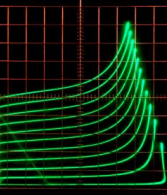






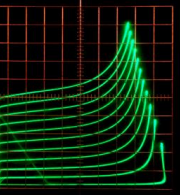
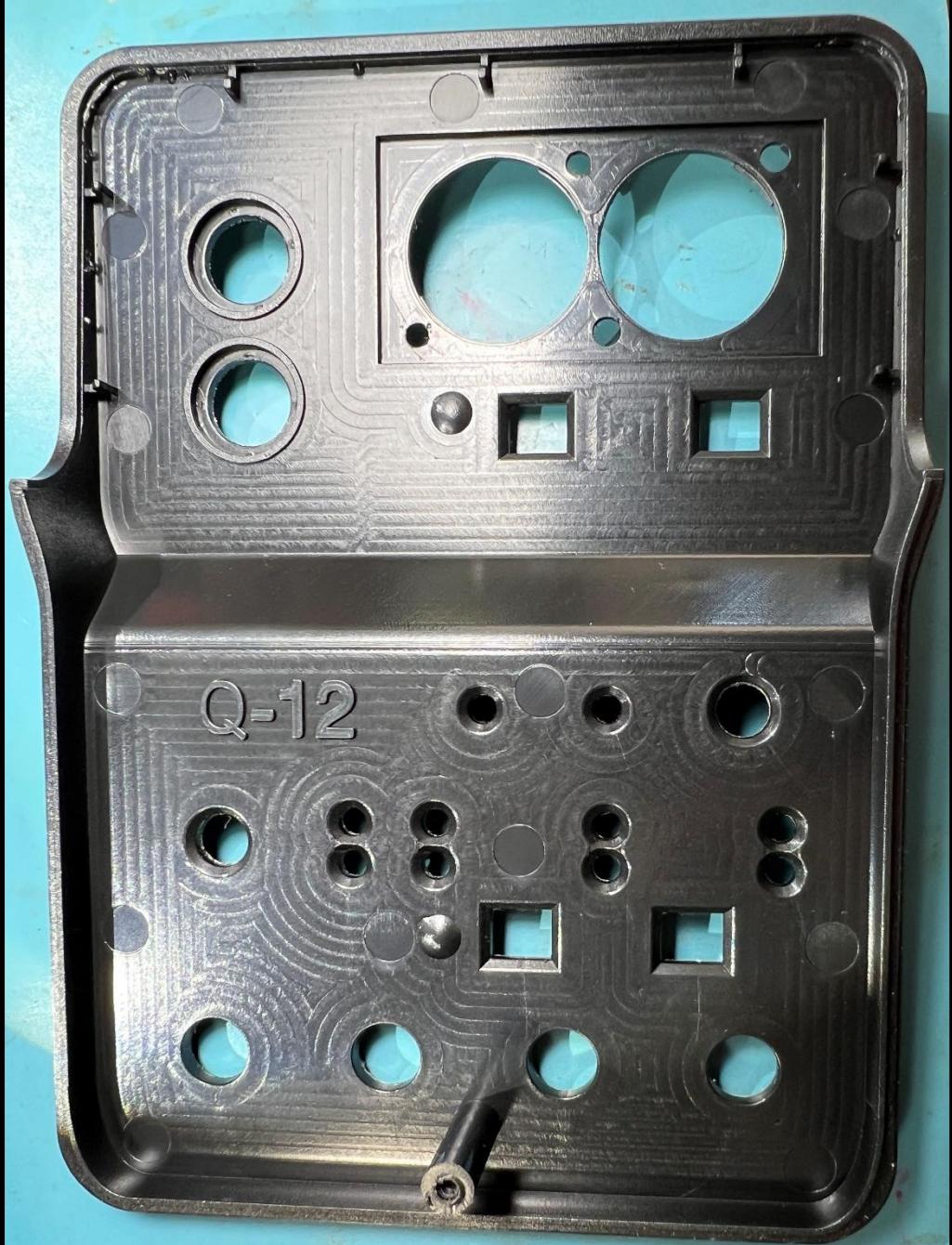
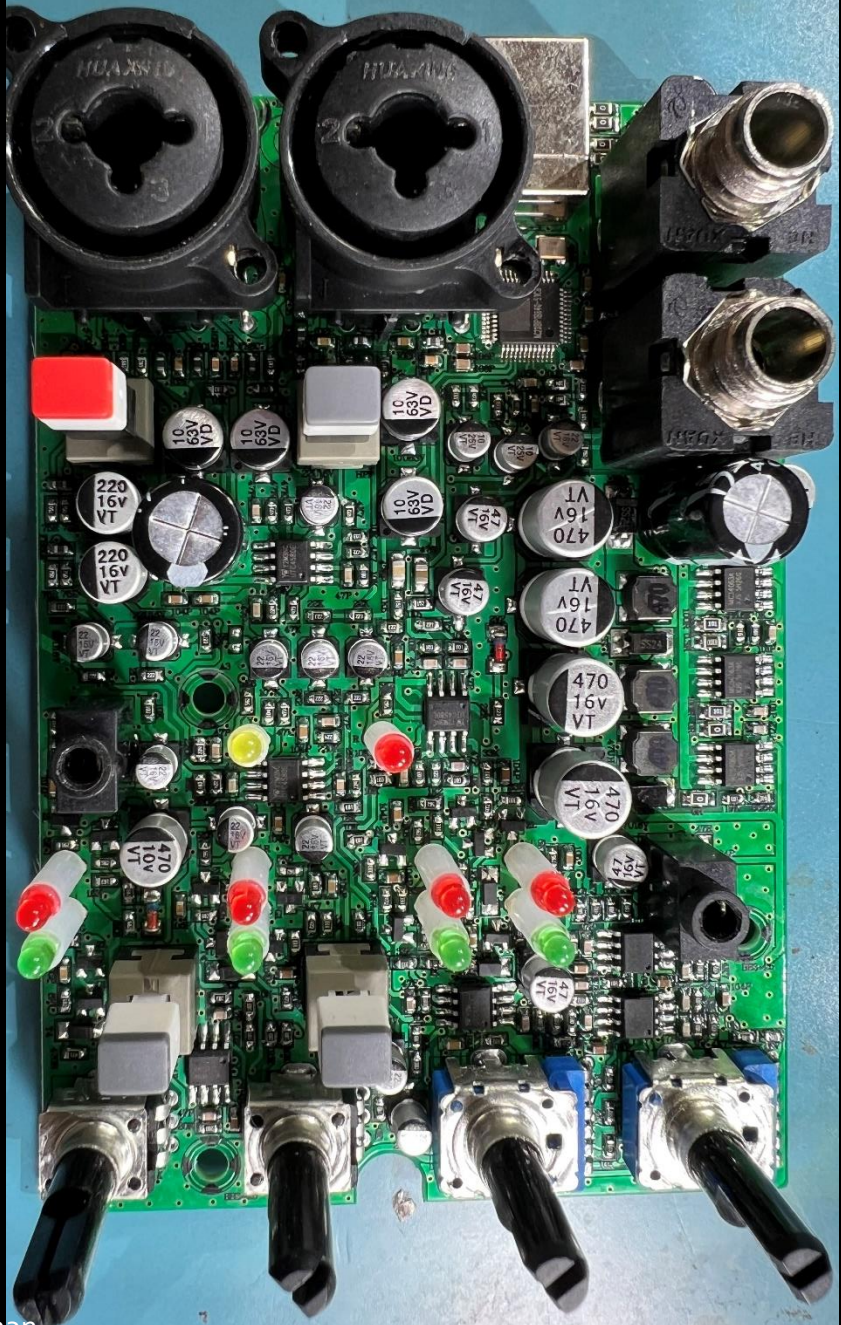


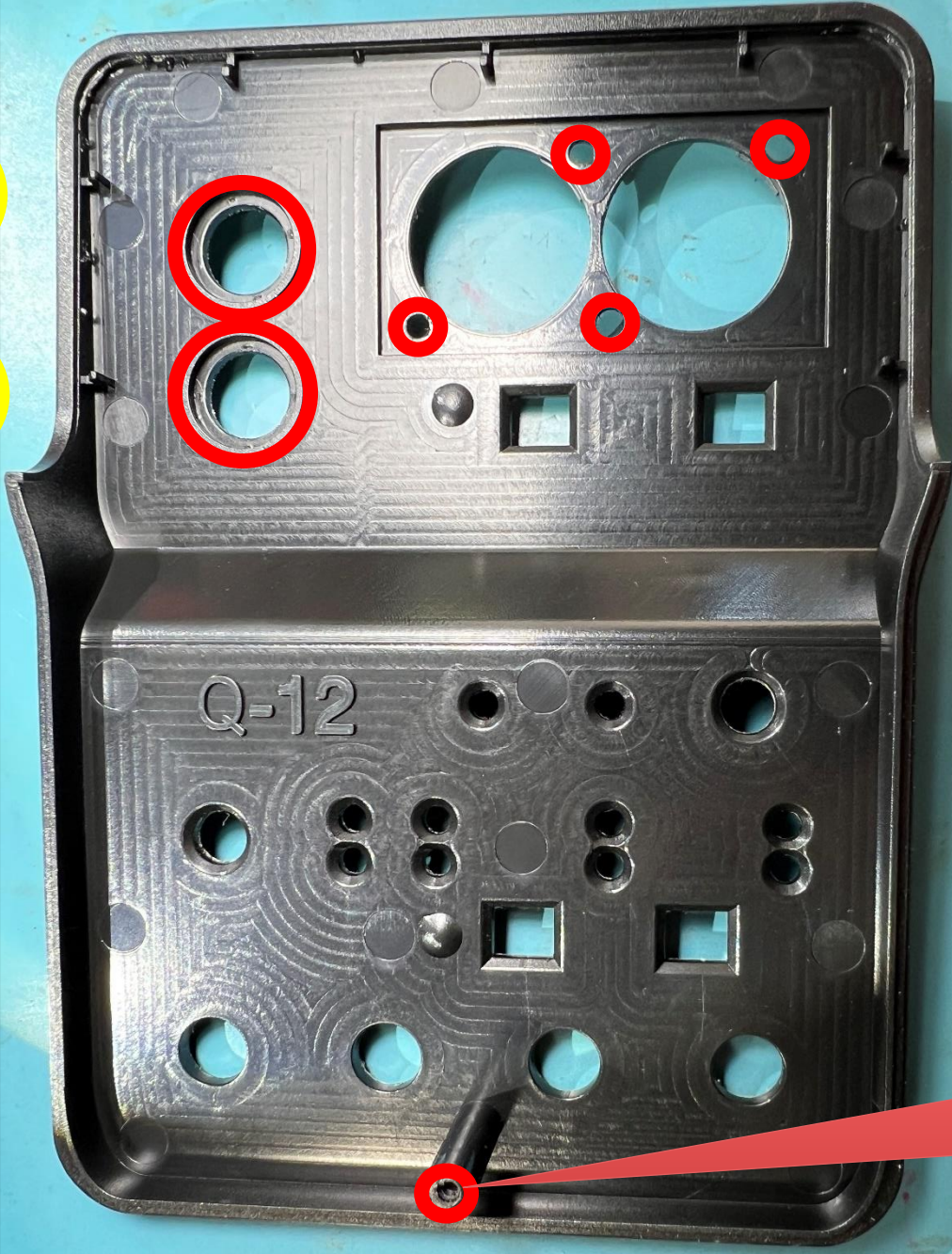
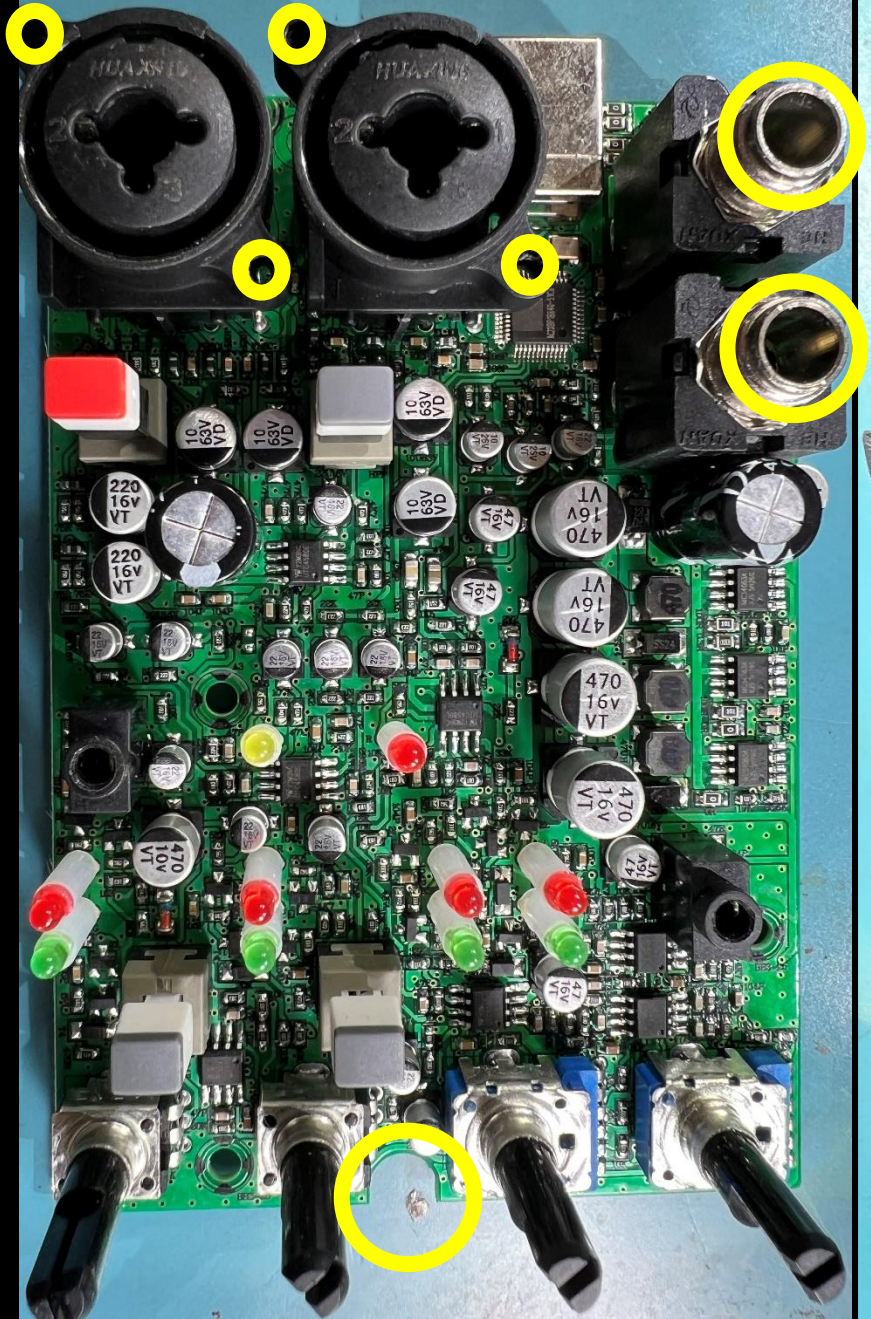
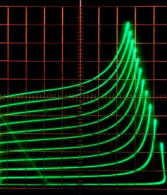




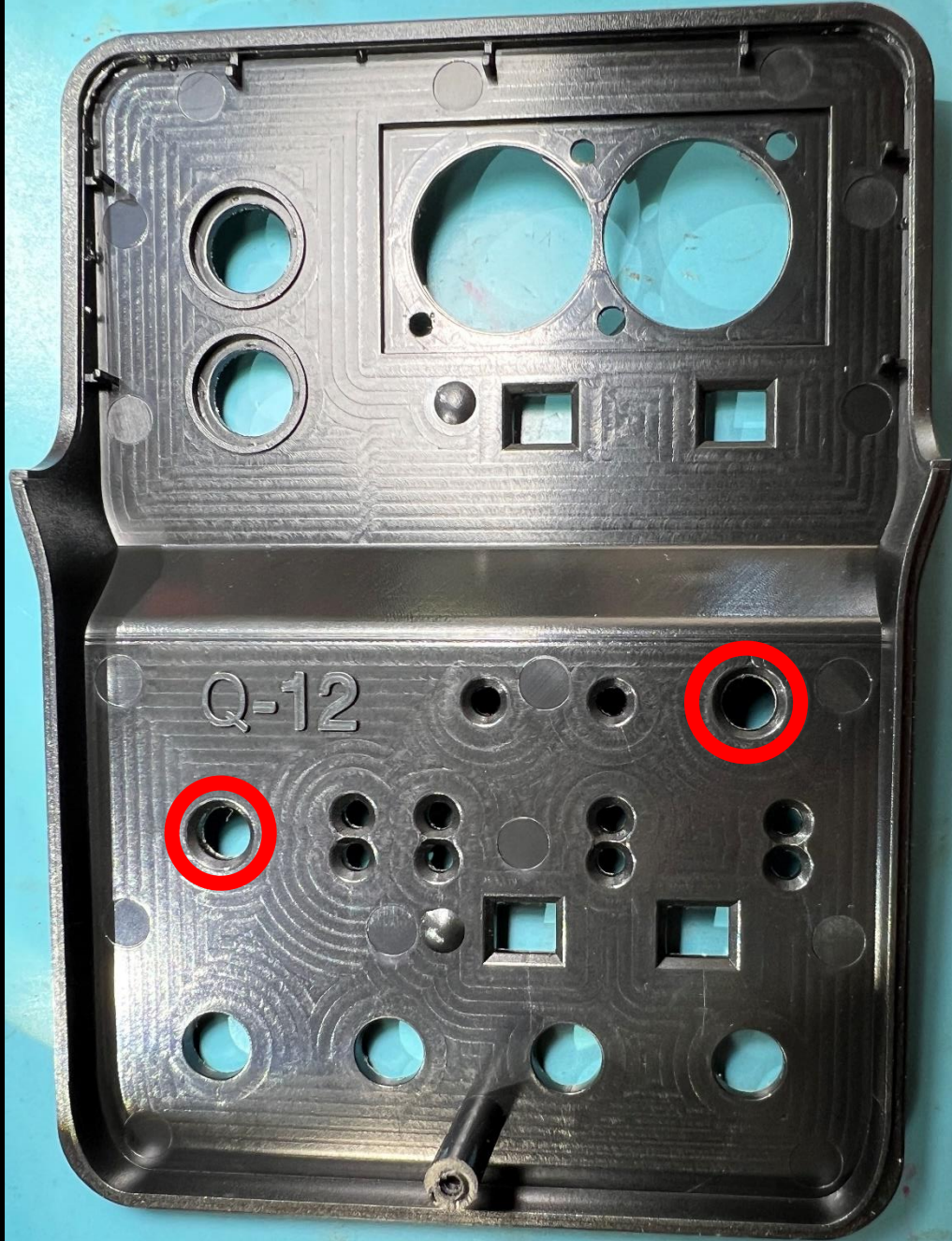
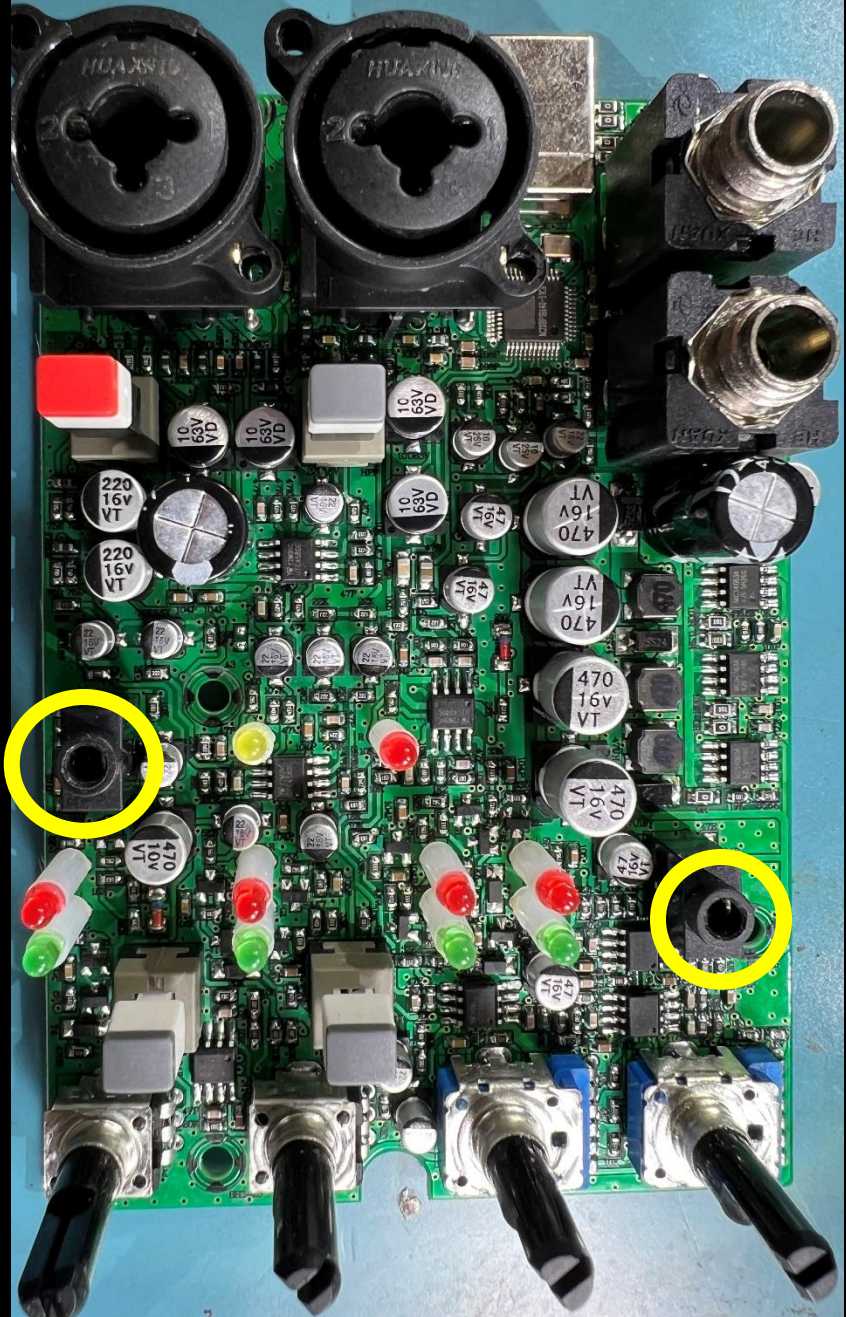
PCB is held down at these 4 points

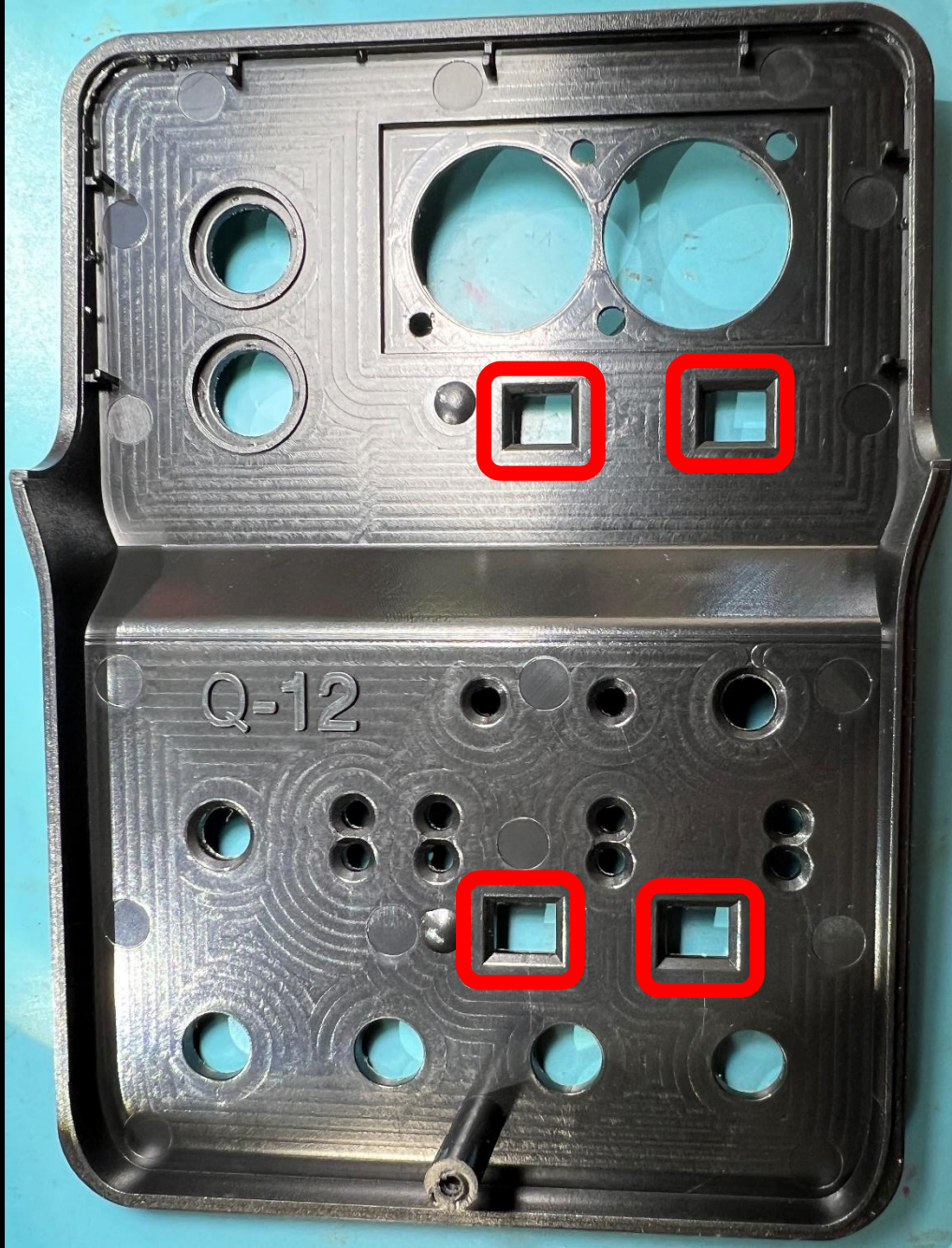
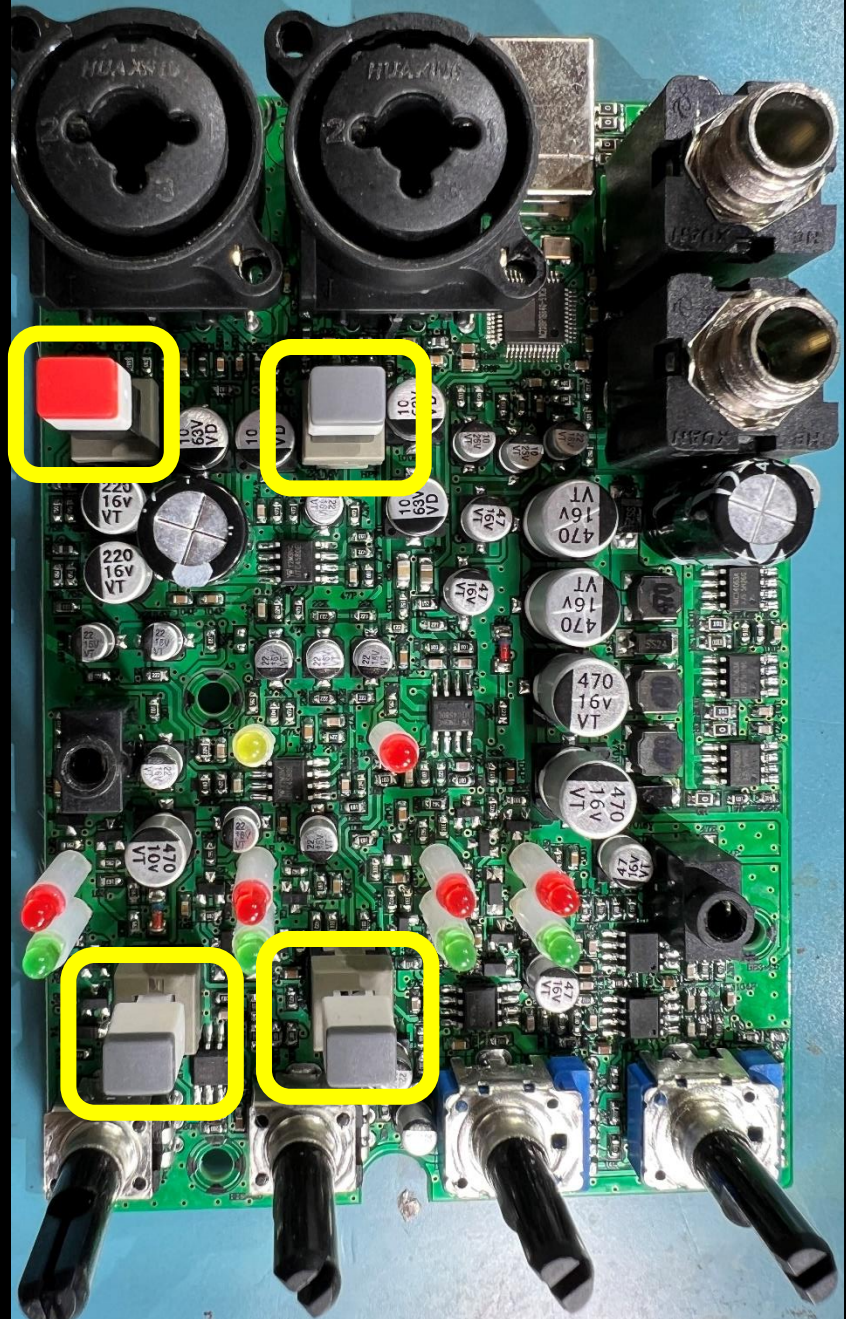
screw hole for the front cover

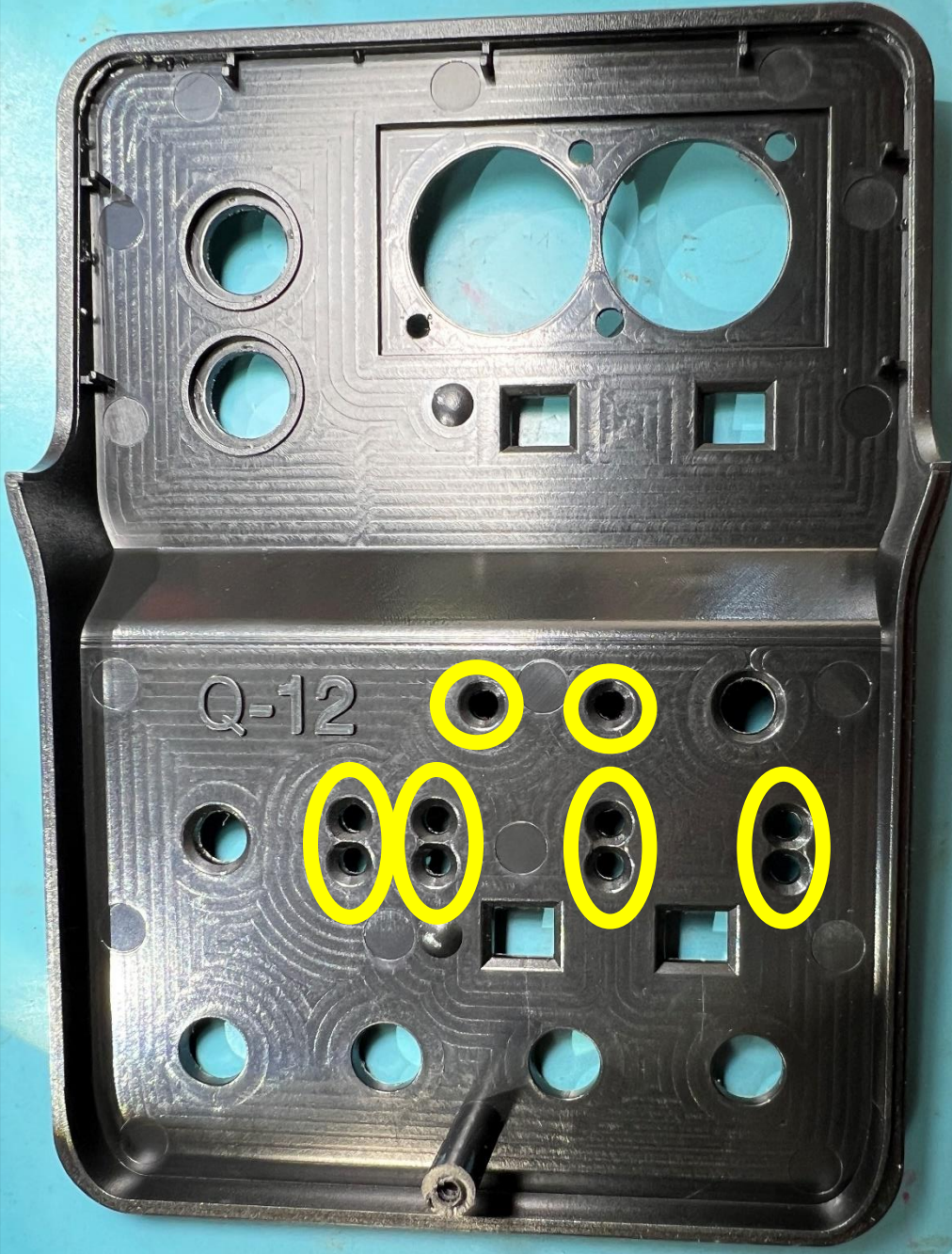


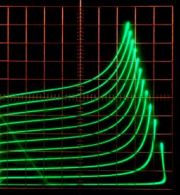


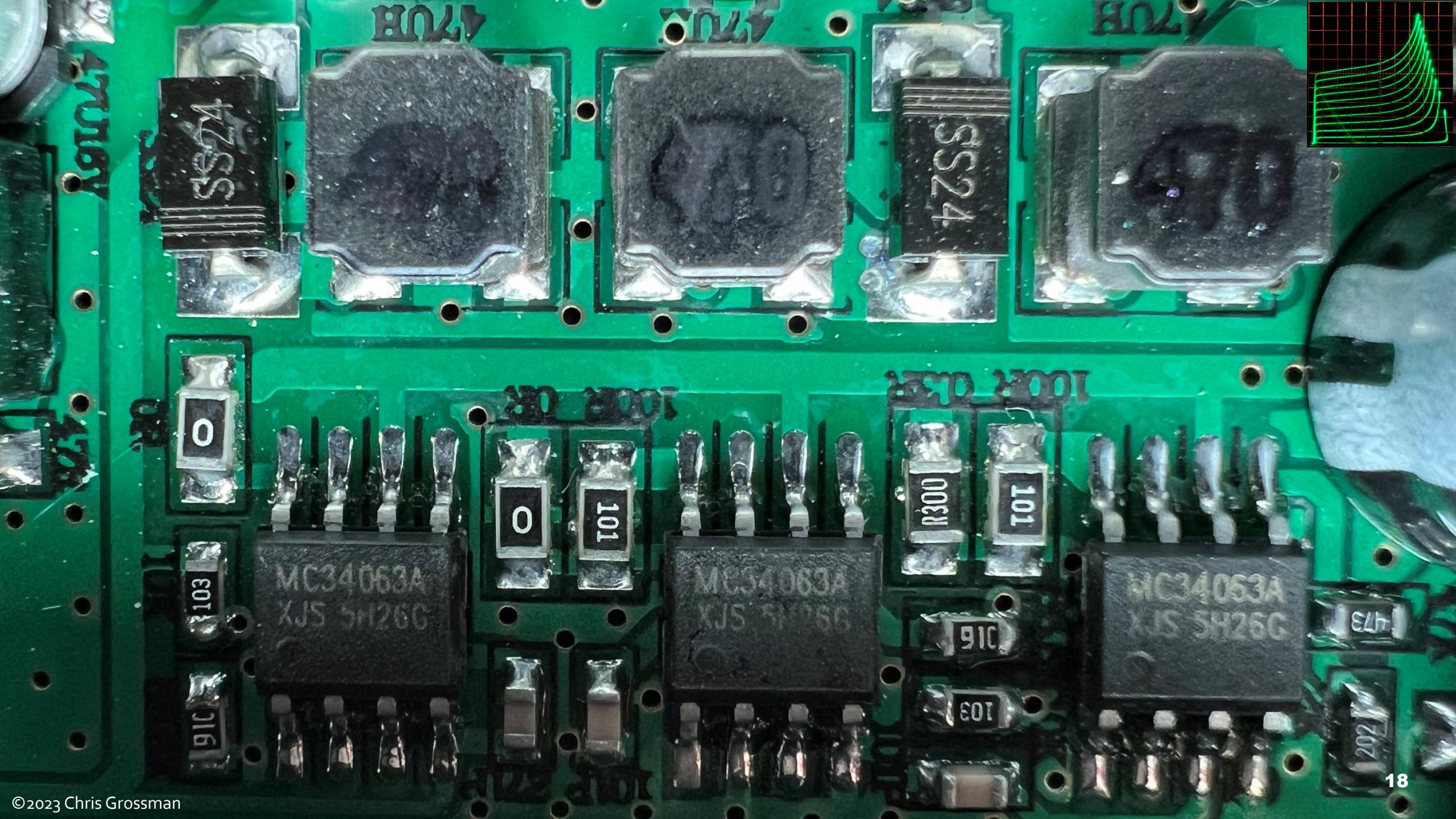
screw goes through the rear cover and attaches here





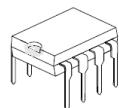






DC TO DC CONVERTER CONTROLLER
DESCRIPTION

The MC34063 is a monolithic regulator subsystem, intended for use as DC to DC converter. This device contains a temperature compensated band gap reference, a duty-cycle control oscillator, driver and high current output switch. It can be used for step down, step-up or inverting switching regulators as well as for series pass regulators.



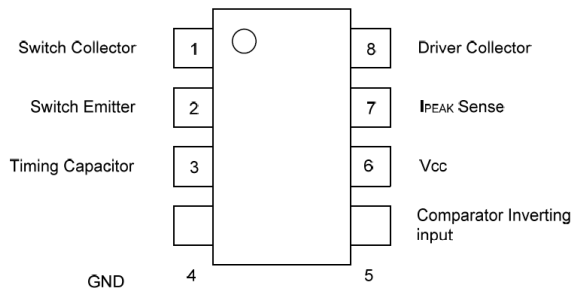
MC34063D DIP-8



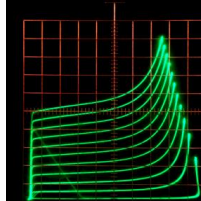
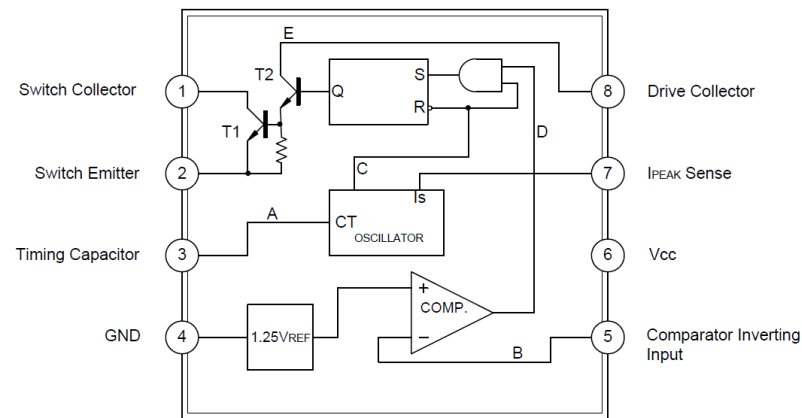
MC34063S SOP-8

FEATURES

- * Operation from 3.0V to 40V.
- * Short circuit current limiting.
- * Low standby current.
- * Output switch current of 1.5A without external transistors.
- * Frequency of operation from 100Hz to 100kHz.
- * Step-up, step-down or inverting switch regulators.

PIN CONFIGURATION

PIN DESCRIPTION

PIN NO	PIN NAME	I/O	DESCRIPTION
1	Switch Collector	I	Internal Darlington pairs T1 collector
2	Switch Emitter	O	Internal Darlington pairs T1 emitter
3	Timing Capacitor		The value of selected capacitor controls the internal oscillator run rate
4	GND		
5	Comparator Inverting Input	I	Inverting input of comparator which can set & initiate the Darlington pairs output switch
6	V _{CC}		
7	I _{PEAK} Sense	I	Current sense input to monitor the voltage drop across an external resistor placed in series with V _{CC}
8	Driver Collector	I	Internal Darlington pairs T2 collector


BLOCK DIAGRAM

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{CC}	40	V
Comparator Input Voltage Range	V _{IN(COMP)}	-0.3 ~ +40	V
Switch Collector Voltage	V _{C(SW)}	40	V
Switch Emitter Voltage	V _{E(SW)}	40	V
Switch Collector to Emitter Voltage	V _{CE(SW)}	40	V
Driver Collector Voltage	V _{CDR}	40	V
Switch Current	I _{SW}	1.5	A
Power Dissipation (Ta=25°C)	P _D	DIP-8	1250
		SOP-8	625
			mW
Junction Temperature	T _J	+150	°C
Operating Temperature	T _{OPR}	0 ~ +70	°C
Storage Temperature	T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction-to-Ambient	θ _{JA}	DIP-8	100
		SOP-8	160
			°C/W

DC-to-DC Converter Control Circuits

The MC34063A Series is a monolithic control circuit containing the primary functions required for DC-to-DC converters. These devices consist of an internal temperature compensated reference, comparator, controlled duty cycle oscillator with an active current limit circuit, driver and high current output switch. This series was specifically designed to be incorporated in Step-Down and Step-Up and Voltage-Inverting applications with a minimum number of external components. Refer to Application Notes AN920A/D and AN954/D for additional design information.

- Operation from 3.0 V to 40 V Input
- Low Standby Current
- Current Limiting
- Output Switch Current to 1.5 A
- Output Voltage Adjustable
- Frequency Operation to 100 kHz
- Precision 2% Reference

MC34063A MC33063A

DC-to-DC CONVERTER CONTROL CIRCUITS

SEMICONDUCTOR TECHNICAL DATA

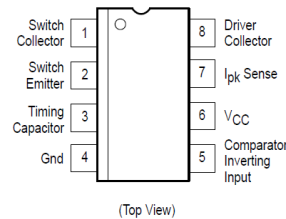


P, P1 SUFFIX
PLASTIC PACKAGE
CASE 626



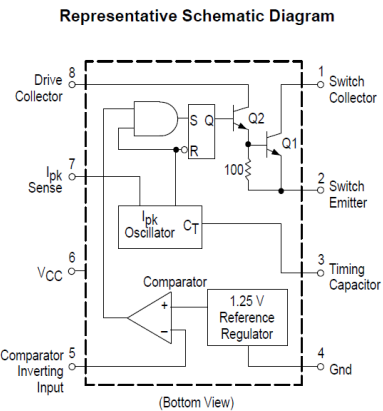
D SUFFIX
PLASTIC PACKAGE
CASE 751
(SO-8)

PIN CONNECTIONS



ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC33063AD	$T_A = -40^\circ \text{ to } +85^\circ \text{C}$	SO-8
MC33063AP1		Plastic DIP
MC33063AVD	$T_A = -40^\circ \text{ to } +125^\circ \text{C}$	SO-8
MC33063AVP		Plastic DIP
MC34063AD	$T_A = 0^\circ \text{ to } +70^\circ \text{C}$	SO-8
MC34063AP1		Plastic DIP



This device contains 51 active transistors.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Power Supply Voltage	V_{CC}	40	Vdc
Comparator Input Voltage Range	V_{IR}	-0.3 to +40	Vdc
Switch Collector Voltage	$V_{C(\text{switch})}$	40	Vdc
Switch Emitter Voltage ($V_{Pin 1} = 40 \text{ V}$)	$V_{E(\text{switch})}$	40	Vdc
Switch Collector to Emitter Voltage	$V_{CE(\text{switch})}$	40	Vdc
Driver Collector Voltage	$V_{C(\text{driver})}$	40	Vdc
Driver Collector Current (Note 1)	$I_{C(\text{driver})}$	100	mA
Switch Current	I_{SW}	1.5	A
Power Dissipation and Thermal Characteristics			
Plastic Package, P, P1 Suffix			
$T_A = 25^\circ \text{C}$	P_D	1.25	W
Thermal Resistance	$R_{\theta JA}$	100	$^\circ \text{C/W}$
SOIC Package, D Suffix			
$T_A = 25^\circ \text{C}$	P_D	625	W
Thermal Resistance	$R_{\theta JA}$	160	$^\circ \text{C/W}$
Operating Junction Temperature	T_J	+150	$^\circ \text{C}$
Operating Ambient Temperature Range			
MC34063A	T_A	0 to +70	$^\circ \text{C}$
MC33063AV		-40 to +125	
MC33063A		-40 to +85	
Storage Temperature Range	T_{stg}	-65 to +150	$^\circ \text{C}$

NOTES: 1. Maximum package power dissipation limits must be observed.
2. ESD data available upon request.

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.0 \text{ V}$, $T_A = T_{low}$ to T_{high} [Note 3], unless otherwise specified.)

Characteristics	Symbol	Min	Typ	Max	Unit
OSCILLATOR					
Frequency ($V_{Pin 5} = 0 \text{ V}$, $C_T = 1.0 \text{ nF}$, $T_A = 25^\circ \text{C}$)	f_{osc}	24	33	42	kHz
Charge Current ($V_{CC} = 5.0 \text{ V}$ to 40 V , $T_A = 25^\circ \text{C}$)	I_{chg}	24	35	42	μA
Discharge Current ($V_{CC} = 5.0 \text{ V}$ to 40 V , $T_A = 25^\circ \text{C}$)	I_{dischg}	140	220	260	μA
Discharge to Charge Current Ratio (Pin 7 to V_{CC} , $T_A = 25^\circ \text{C}$)	I_{dischg}/I_{chg}	5.2	6.5	7.5	-
Current Limit Sense Voltage ($I_{chg} = I_{dischg}$, $T_A = 25^\circ \text{C}$)	$V_{ipk(\text{sense})}$	250	300	350	mV

OUTPUT SWITCH (Note 4)

Saturation Voltage, Darlington Connection (Note 5) ($I_{SW} = 1.0 \text{ A}$, Pins 1, 8 connected)	$V_{CE(\text{sat})}$	-	1.0	1.3	V
Saturation Voltage, Darlington Connection ($I_{SW} = 1.0 \text{ A}$, $R_{Pin 8} = 82 \Omega$ to V_{CC} , Forced $\beta \approx 20$)	$V_{CE(\text{sat})}$	-	0.45	0.7	V
DC Current Gain ($I_{SW} = 1.0 \text{ A}$, $V_{CE} = 5.0 \text{ V}$, $T_A = 25^\circ \text{C}$)	h_{FE}	50	75	-	-
Collector Off-State Current ($V_{CE} = 40 \text{ V}$)	$I_{C(\text{off})}$	-	0.01	100	μA

NOTES: 3. $T_{low} = 0^\circ \text{C}$ for MC34063A, -40°C for MC33063A, AV $T_{high} = +70^\circ \text{C}$ for MC34063A, $+85^\circ \text{C}$ for MC33063A, $+125^\circ \text{C}$ for MC33063AV
4. Low duty cycle pulse techniques are used during test to maintain junction temperature as close to ambient temperature as possible.
5. If the output switch is driven into hard saturation (non-Darlington configuration) at low switch currents ($\leq 300 \text{ mA}$) and high driver currents ($\geq 30 \text{ mA}$), it may take up to 2.0 μs for it to come out of saturation. This condition will shorten the off time at frequencies $\geq 30 \text{ kHz}$, and is magnified at high temperatures. This condition does not occur with a Darlington configuration, since the output switch cannot saturate. If a non-Darlington configuration is used, the following output drive condition is recommended:

$$\text{Forced } \beta \text{ of output switch: } \frac{I_{C \text{ output}}}{I_{C \text{ driver}}} \geq 10$$

*The 100 Ω resistor in the emitter of the driver device requires about 7.0 mA before the output switch conducts.



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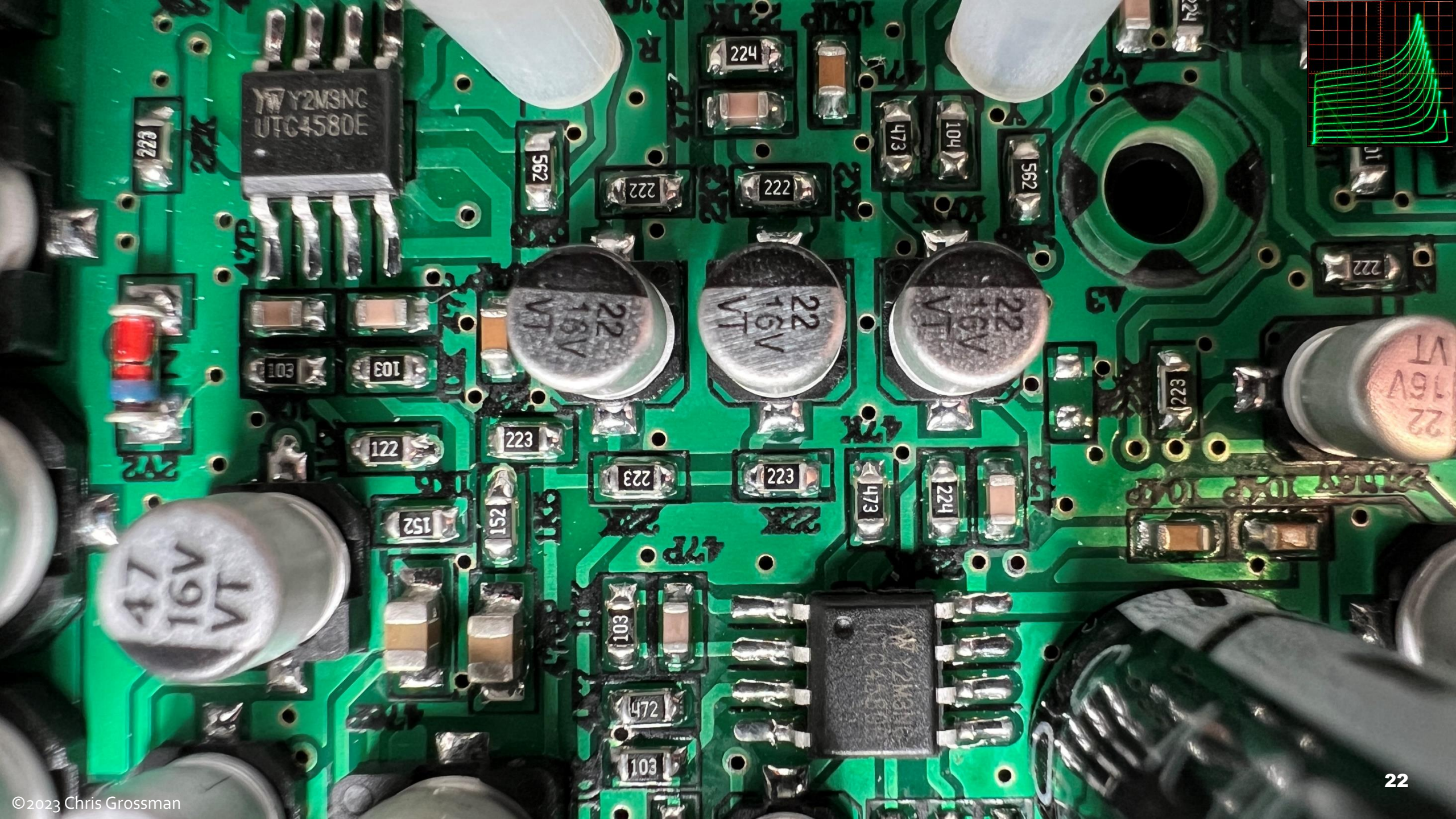
Store rating	On-time delivery rate
4.7/5	97.6%
Response time	Online revenue
≤7h	\$280,000+

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DUAL OPERATIONAL AMPLIFIER

DESCRIPTION

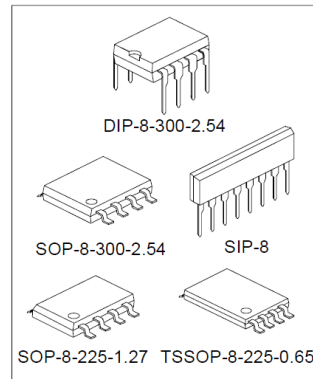
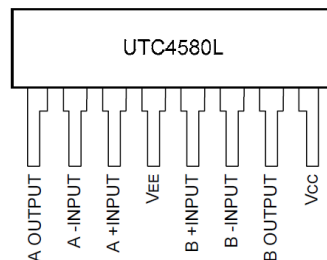
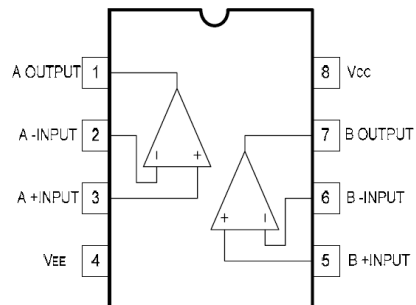
UTC4580 is the dual operational amplifier, specially designed for improving the tone control, which is most suitable for the audio application.

Featuring noiseless, higher gain bandwidth, high output current and low distortion ratio, and it is most suitable not only for acoustic electronic parts of audio per-amp and active filter, but also for the industrial measurement tools. It is also suitable for the head phone amp at higher output current, and further more, it can be applied for the handy type set operational amplifier of general purpose in application of low voltage single supply type which is properly biased of the input low voltage source.

FEATURES

- * Operating voltage ($\pm 2 \sim \pm 18V$)
- * Low input noise voltage ($0.8\mu V_{rms}$ typ.)
- * Wide gain bandwidth produce ($15MHz$ typ.)
- * Low distortion (0.0005% typ.)
- * Slew rate ($5V/\mu s$ typ.)
- * Package outline SOP8, SIP-8, DIP-8, TSSOP-8

PIN CONFIGURATION



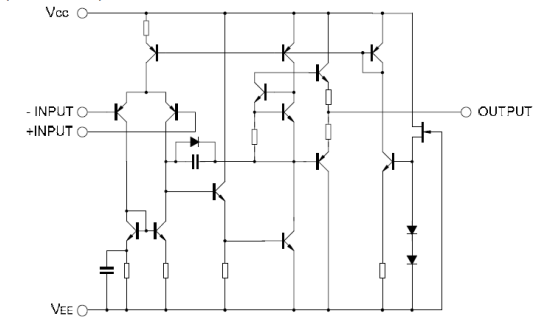
ORDERING INFORMATION

Device	package
UTC4580M	SOP-8-300-1.27
UTC4580L	SIP-8
UTC4580	DIP-8-300-2.54
UTC4580E	SOP-8-225-1.27
UTC4580V	TSSOP-8-225-0.65

AP PLICATIONS

- * Audio per-amp;
- * Head phone amp;
- * Handy type set;
- * Measurement tool;

BLOCK DIAGRAM (1/2 Shown)

ABSOLUTE MAXIMUM RATINGS ($T_{amb}=25^{\circ}C$)

Characteristic	Symbol	Value	Unit
Differential Input Voltage	V_{+}/V_{-}	± 18	V
Supply Voltage	V_{IC}	± 15 (note)	V
Input Voltage	V_{ID}	± 30 (note)	V
Output Current	I_O	± 50	mA
Power Dissipation	PD	(UTC4580) 800 (UTC4580L) 800 (UTC4580M) 350 (UTC4580E) 300 (UTC4580V) 250	mW
Operating Temperature Range	T_{opr}	$-20 \sim +75$	$^{\circ}C$
Storage Temperature Range	T_{stg}	$-20 \sim +125$	$^{\circ}C$

ELECTRICAL CHARACTERISTICS ($T_{amb}=25^{\circ}C, V_{+}/V_{-} = \pm 15$)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Input Offset Voltage	V_{IO}	$R_s \leq 10k\Omega$	-	0.5	3	mV
Input Offset Current	I_{IO}		-	5	200	nA
Input Bias Current	I_{IS}		-	100	500	nA
Large Signal Voltage Gain	A_V	$R_L \geq 2k\Omega, V_O = \pm 10V$	90	110	-	dB
Output Voltage Swing	V_{OM}	$R_L \geq 2k\Omega$	± 12	± 13.5	-	V
Input Common Mode Voltage Range	V_{ICM}		± 12	± 13.5	-	V
Common Mode Rejection Ratio	CMR	$R_s \leq 10k\Omega$	80	110	-	dB
Supply Voltage Rejection Ratio	SVR	$R_s \leq 10k\Omega$	90	110	-	dB
Operating Current	ICC		-	6	9	mA
Slew Rate	SR	$R_L \geq 2k\Omega$	-	5	-	V/ μs
Gain Bandwidth Product	GB	$f = 10kHz$	-	15	-	MHz
Total Harmonic Distortion	THD	$A_V = 20dB, V_O = 5V, R_L = 2k\Omega, f = 1kHz$	-	0.0005	-	%
Input Noise Voltage	VNI	RIAA $R_s = 2.2k\Omega, 30kHz LPF$	-	0.8	-	μV_{rms}

DUAL OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJM4580 is a dual operational amplifier, specially designed for improving the tone control, which is most suitable for the audio application.

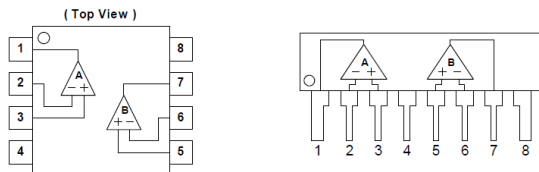
Featuring noiseless, higher gain bandwidth, high output current and low distortion ratio, and it is most suitable not only for acoustic electronic parts of audio pre-amp and active filter, but also for the industrial measurement tools. It is also suitable for the head phone amp at higher output current, and further more, it can be applied for the handy type set operational amplifier of general purpose in application of low voltage single supply type which is properly biased of the low voltage source.

The D-Rank type products(NJM4580DD/LD/MD/ED) have specified maximum limits for equivalent input noise voltage.

■ FEATURES

- Operating Voltage $\pm 2V \sim \pm 18V$
- Low Input Noise Voltage $0.8\mu V_{rms}$ typ. (RIAA)
- Wide GBW $15MHz$ typ.
- Low Distortion 0.0005% typ.
- Slew Rate $5V/\mu s$ typ.
- Bipolar Technology
- Package Outline
 DIP8, SIP8, DMP8, SSOP8, MSOP8(VSP8) MEET JEDEC MO-187-DA
 SOP8 JEDEC 150mil

■ PIN CONFIGURATION

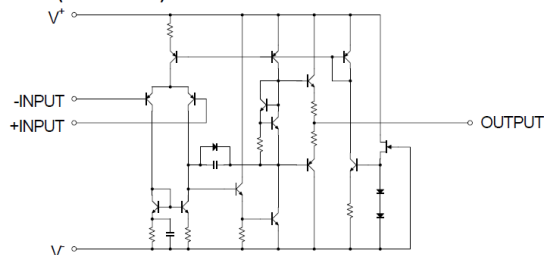


NJM4580D, NJM4580M, NJM4580E
NJM4580V, NJM4580R

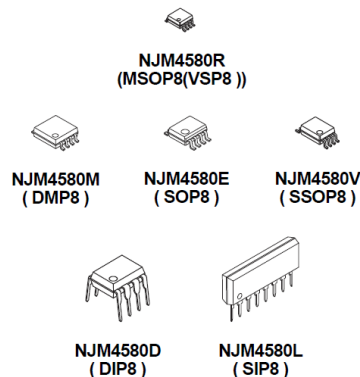
NJM4580L

- PIN FUNCTION**
1. A OUTPUT
 2. A - INPUT
 3. A + INPUT
 4. V⁻
 5. B + INPUT
 6. B - INPUT
 7. B OUTPUT
 8. V⁺

■ EQUIVALENT CIRCUIT (1/2 Shown)



■ PACKAGE OUTLINE



■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C, unless otherwise noted.)

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage	V ⁺ /V ⁻	±18	V
Input Voltage	V _{ICM}	±15 (Note1)	V
Differential Input Voltage	V _{ID}	±30 (Note1)	V
Power Dissipation	P _D	DIP8, SIP8 : 800 DMP8, SOP8 : 300 SSOP8 : 250 MSOP8(VSP8) : 400 (Note2)	mW
Operating Temperature Range	T _{opr}	-40~+85	°C
Storage Temperature Range	T _{stg}	-40~+125	°C

(Note1) For supply voltage less than ±15V, the absolute maximum input voltage is equal to supply voltage.

(Note2) On the PCB "EIA/JEDEC (114.3×76.2×1.57mm, 2 layers, FR-4)"

■ RECOMMENDED OPERATING CONDITIONS (Ta=25°C)

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V ⁺ /V ⁻		±2	-	±18	V

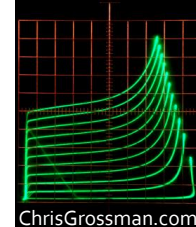
■ ELECTRICAL CHARACTERISTICS (V⁺/V⁻=±15V, Ta=25°C, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V _{IO}	R _S ≤10kΩ	-	0.3	3	mV
Input Offset Current	I _{IO}		-	5	200	nA
Input Bias Current	I _B		-	100	500	nA
Voltage Gain	A _V	R _L ≥2kΩ, V _O =±10V	90	110	-	dB
Maximum Output Voltage	V _{OM}	R _L ≥2kΩ	±12	±13.5	-	V
Common Mode Input Voltage Range	V _{ICM}		±12	±13.5	-	V
Common Mode Rejection Ratio	CMR	R _S ≤10kΩ	80	110	-	dB
Supply Voltage Rejection Ratio	SVR	R _S ≤10kΩ	80	110	-	dB
Supply Current	I _{CC}		-	6	9	mA
Slew Rate	SR	R _L ≥2kΩ	-	5	-	V/μs
Gain Bandwidth Product	GB	f=10kHz	-	15	-	MHz
Total Harmonic Distortion	THD	A _V =20dB, V _O =5V, R _L =2kΩ, f=1kHz	-	0.0005	-	%
Equivalent Input Noise Voltage	V _N	RIAA, R _S =2.2kΩ, 30kHz LPF	-	0.8	-	μVrms

■ ELECTRICAL CHARACTERISTICS (D-rank type(Note3), V⁺/V⁻=±15V, Ta=25°C, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Equivalent Input Noise Voltage	V _N	RIAA, R _S =2.2kΩ	-	-	1.4	μVrms

(Note3)D-rank type is a Equivalent Input Noise Voltage selected product. It's only DIP, DMP, SOP and SIP package.



ChrisGrossman.com



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\$0.10 - \$1.00 / piece | 10 piece/pieces (Min. order)

Benefits: US \$500 coupons [Claim now >](#)

Package: **SOP-8**

D/C: **last year**

Specification: Original Product \$0.10

Samples: SOP-8, last year, Original Product
\$1.00/piece Min. order : 1 piece [Get samples](#)

Lead time: ⓘ

Quantity (pieces)	1 - 5000	5001 - 20000	20001 - 50000	> 50000
Lead time (days)	1	2	3	To be negotiated

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Enjoy **On-time Dispatch Guarantee** ⓘ

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Lead time 1 days ⓘ

Shipping: To be negotiated

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Shenzhen Keshijin Electronics Co., L...
Multispecialty supplier
CN 2 YRS [VR Showroom](#)

Store rating: **4.9/5** | On-time delivery rate: **98.4%**
Response time: **≤1h** | Online revenue: **\$50,000+**
Main markets: Collaborating suppl...
North America, ... 28

Services:
Full customization
On-site technical support

Quality control:
Raw-material traceability identifi...
Testing instruments (1)

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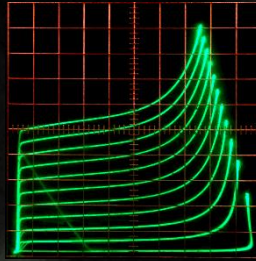
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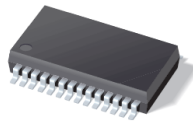
TOP



48 pin package
Part # lasered off

24 MHz crystal
1 side connects pin 15

Pin 1



PCM2901
PCM2903

SLES034C—MARCH 2002—REVISED NOVEMBER 2007

STEREO AUDIO CODEC WITH USB INTERFACE, SINGLE-ENDED ANALOG INPUT/OUTPUT AND S/PDIF

FEATURES

- **PCM2901:** Without S/PDIF
- **PCM2903:** With S/PDIF
- **On-Chip USB Interface**
 - With Full-Speed Transceivers
 - Fully Compliant With USB 1.1 Specification
 - Certified by USB-IF
 - Partially Programmable Descriptors ⁽¹⁾
 - USB Adaptive Mode for Playback
 - USB Asynchronous Mode for Record
 - Self-Powered
- **16-Bit Delta-Sigma ADC and DAC**
- **Sampling Rates**
 - DAC: 32, 44.1, 48 kHz
 - ADC: 8, 11.025, 16, 22.05, 32, 44.1, 48 kHz
- **On-Chip Clock Generator With Single 12-MHz Clock Source**
- **Single Power Supply: 3.3 V Typical**
- **Stereo ADC**
 - Analog Performance at $V_{CC} = V_{CCP1} = V_{CCP2} = V_{CCX} = V_{DD} = 3.3 V$
 - THD+N = 0.01%
 - SNR = 89 dB
 - Dynamic Range = 89 dB
 - Decimation Digital Filter
 - Pass-Band Ripple = ± 0.05 dB
 - Stop-Band Attenuation = -65 dB
 - Single-Ended Voltage Input
 - Antialiasing Filter Included
 - Digital LCF Included

- **Stereo DAC**
 - Analog Performance at $V_{CC} = V_{CCP1} = V_{CCP2} = V_{CCX} = V_{DD} = 3.3 V$
 - THD+N = 0.005%
 - SNR = 96 dB
 - Dynamic Range = 93 dB
 - Oversampling Digital Filter
 - Pass-Band Ripple = ± 0.1 dB
 - Stop-Band Attenuation = -43 dB
 - Single-Ended Voltage Output
 - Analog LPF Included
- **Multifunctions**
 - Human Interface Device (HID) Volume \pm Control and Mute Control
 - Suspend Flag
- **Package: 28-Pin SSOP**

APPLICATIONS

- USB Audio Speaker
- USB Headset
- USB Monitor
- USB Audio Interface Box

DESCRIPTION

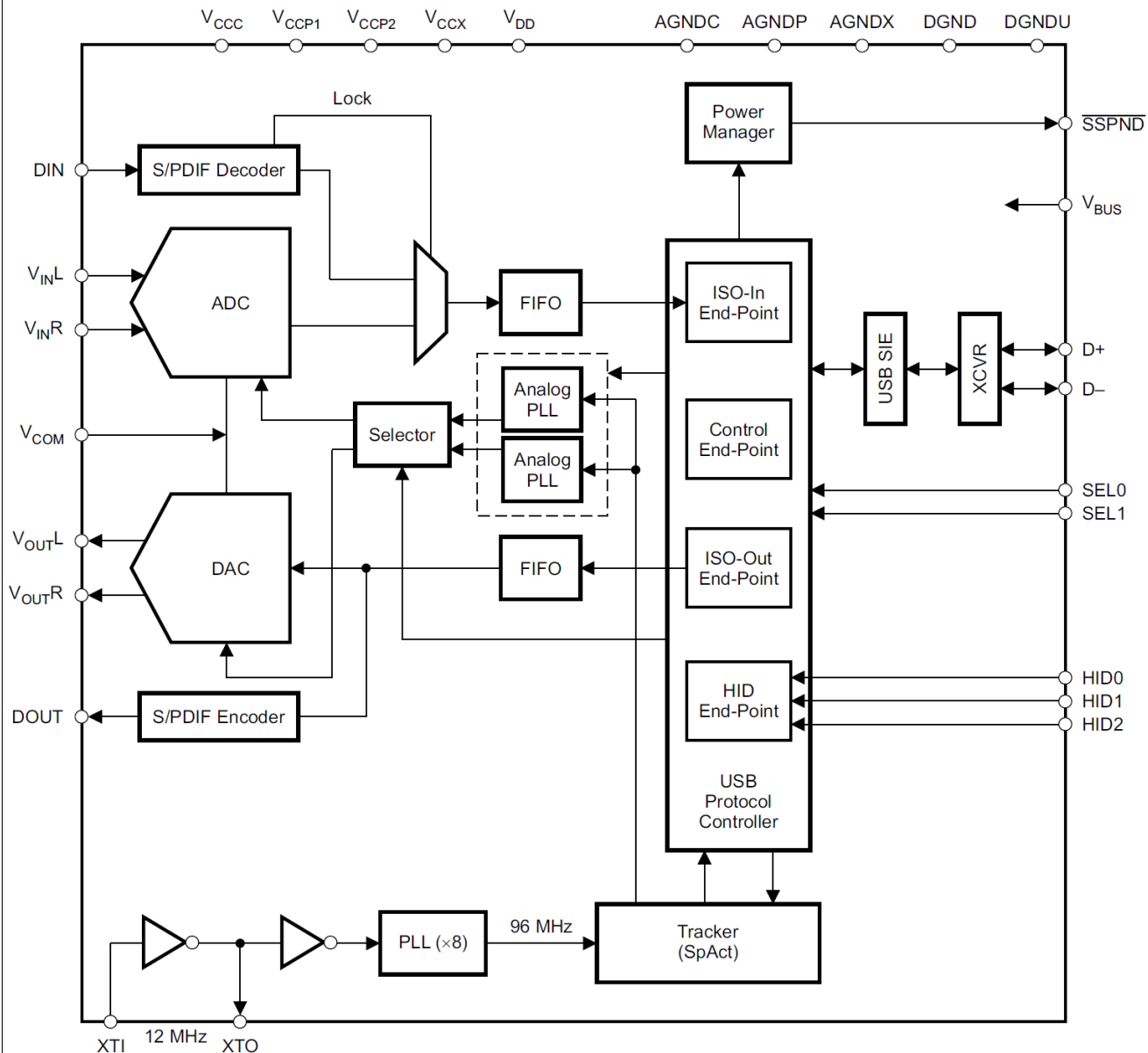
The PCM2901/2903 is TI's single-chip USB stereo audio codec with USB-compliant full-speed protocol controller and S/PDIF (only PCM2903). The USB protocol controller works with no software code, but the USB descriptors can be modified in some areas (for example, vendor ID/product ID). The PCM2901/2903 employs SpAct™ architecture, TI's unique system that recovers the audio clock from USB packet data. On-chip analog PLLs with SpAct enable playback and record with low clock jitter and with independent playback and record sampling rates.

(1) The descriptor can be modified by changing a mask.

PCM2901
PCM2903

SLES034C—MARCH 2002—REVISED NOVEMBER 2007

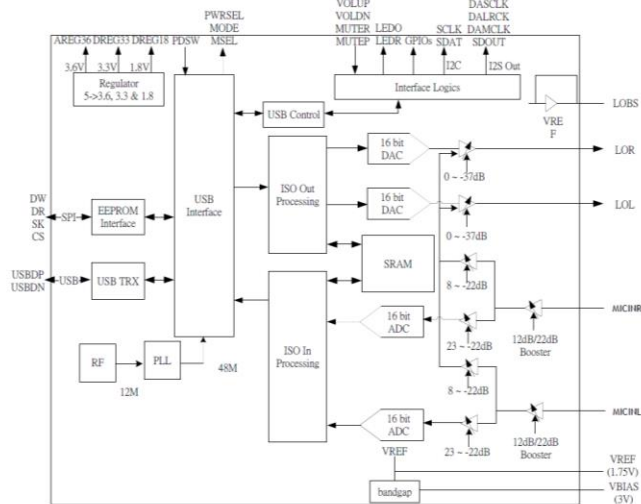
PCM2903 FUNCTIONAL BLOCK DIAGRAM



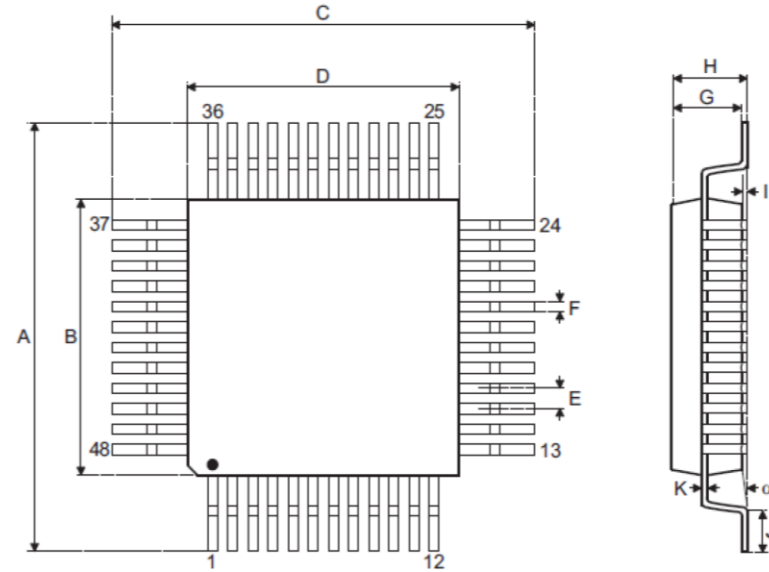
DESCRIPTION

The CM118B is a highly integrated, **crystal-less** USB audio single chip solution optimized for USB headset, headphone, dongle, microphone and application such as VoIP (voice over Internet protocol). All essential analog modules are embedded in the CM118B, including dual DAC and ADC, earphone driver, microphone booster, PLL, regulator, and USB transceiver. It also supports 3 GPIO pins. In addition, audio adjustment can be easily controlled via specific HID compliant volume control pins. Many features such as headset, headphone and microphone only topologies are programmable with jumper pins. Vender can customize unique USB VID / PID / Product String / Manufacture String and min/max/initial volumes to the external EEPROM. The CM118B also offers anti-pop noise circuits design and internal oscillator which can operate without an external crystal oscillator.

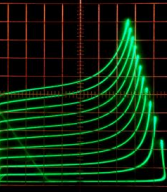
BLOCK DIAGRAM



6. Package dimensions



Symbol	Dimensions in mm		
	Minimum	Normal	Maximum
A	8.90	—	9.10
B	6.90	—	7.10
C	8.90	—	9.10
D	6.90	—	7.10
E	—	0.50	—
F	—	0.20	—
G	1.35	—	1.45
H	—	—	1.60
I	—	0.10	—
J	0.45	—	0.75
K	0.10	—	0.20
α	0°	—	7°



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Package: 48-LQFP

D/C: new

Specification: CM108B \$0.90

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Quantity (pieces)	1 - 500	> 500
Lead time (days)	3	To be negotiated

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Customized packaging (Min. order 10000 pieces)
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The minimum order quantity is 1 piece
0/1 piece from **\$0.90**
Lead time **3 days**

Shipping: To be negotiated

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Verified supplier

Shenzhen Yixing Micro Technology...
Multispecialty supplier
CN 2 YRS

Store rating: **5.0/5** | On-time delivery rate: **100.0%**
Response time: **≤1h** | Online revenue: **\$70,000+**

Main markets: **Domestic Mark...**

Quality control: **QA/QC inspectors (1)**
Testing instruments (1)

Certifications: [Certificates](#)

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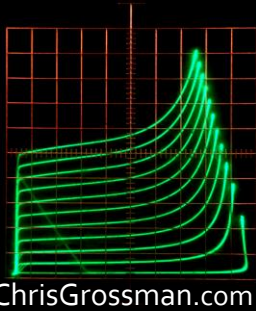
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Teyun Q-12 Teardown Conclusion



- 2-sided PCB
 - A well engineered compact layout
 - All in-between spaces on both sides are ground/power fill
- 3 types of low-cost ICs
 1. (7) UTC4850 dual 15 MHZ bipolar op-amps
 2. (3) MC34063A bipolar switching regulators
 3. (1) USB – ADC – DAC interface of unknown origin
- The quality of components seems excellent (considering the price)
- Well engineered molded case & mechanical assembly
 - No shielding from the case
 - Designed for easy assembly with a minimum number of fasteners
- It is flawed but still useful
 - It really needs more another 20dB of gain
 - Flaccid 48V phantom power (essentially useless)
 - I'm using it to record the (view chart) sound for this video with my EV 664A microphone



The total manufacturing cost for unit is probably < \$5
It is flawed, but well engineered for the price.