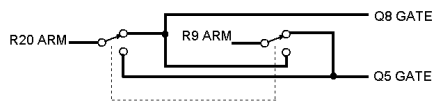


\* LOW FREQ. ENHANCEMENT #1  
 Remove L3, L4, L5, L6, R26, R27.  
 Increase C8, C16, C18 to 0.01 uF.  
 Increase C12 to 820 pF.

! LOW FREQ. ENHANCEMENT #2 (SWAP SWITCH)  
 Disconnect R20 arm from Q8 gate, R9 arm from Q5 gate.  
 Install DPDT switch as shown:



RESISTANCE VALUES ARE IN OHMS  
 CAPACITANCE VALUES ARE IN MICROFARADS  
 UNLESS OTHERWISE SPECIFIED

MODEL NO.	REVISION	PCB P/N	Sheet	of 1
MFJ-1026	2	862-1026		
DESCRIPTION				
NOISE CANCELING SIGNAL ENHANCER				
DATE	PC BOARD	MFJ ENTERPRISES, INC.		
04/21/97	M1026_9	300 INDUSTRIAL PARK RD		
DESIGNED BY	STARKVILLE, MS 39759			
K. BUSH				